# EECS 219C: Computer-Aided Verification Explicit-State Model Checking: Additional Material 

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## Checking if M satisfies $\phi$ : Steps

1. Compute Buchi automaton B corresponding to ~
2. Compute the Buchi automaton $A$ corresponding to the system M
3. Compute the synchronous product P of A and B

- Product computation defines "accepting" states of $P$ based on those of $B$

4. Check if some "accepting" state of $P$ is visited infinitely often

- If so: we found a bug
- If not, no bug in M


## Example of Step 2



Kripke structure

## What's different between

 the two? What's the same?

Corresponding Buchi automaton (transitions on labels not shown go to a non-accepting sink state "err") ${ }_{3}$

## Step 1: Buchi Automaton from Kripke Structure

- Given: Kripke structure $\mathrm{M}=\left(\mathrm{S}, \mathrm{S}_{0}, \mathrm{R}, \mathrm{L}\right)$
$-L: S \rightarrow 2^{A P}, A P-s e t$ of atomic propositions
- Construct Buchi automaton
$A=\left(\Sigma, S \cup\left\{\alpha_{0}, e r r\right\}, \Delta,\left\{\alpha_{0}\right\}, S \cup\left\{\alpha_{0}\right\}\right)$ where:
- Alphabet, $\Sigma=2^{\mathrm{AP}}$
- Set of states $=\mathrm{S} \cup\left\{\alpha_{0}, \mathrm{err}\right\}$
- $\alpha_{0}$ is a special start state, err is a (sink) error state
- All states are accepting except err
- $\Delta$ is transition relation of $A$ such that:
- $\Delta\left(s, \sigma, s^{\prime}\right)$ iff $R\left(s, s^{\prime}\right)$ and $\sigma=L\left(s^{\prime}\right)$
- $\Delta\left(\alpha_{0}, \sigma, s\right)$ iff $s \in S_{0}$ and $\sigma=L(s)$


## Step 2: Compute synchronous product of $A$ with $B$

- $A$ and $B$ are both Buchi automata with the same alphabet
- Synchronous product:
$-\mathrm{A}=\left(\Sigma, \mathrm{S}_{1}, \Delta_{1},\left\{\mathrm{~S}_{0}\right\}, \mathrm{S}_{1} \backslash\{\mathrm{err}\}\right) \quad$ (err is dummy error state)
$-\mathrm{B}=\left(\Sigma, \mathrm{S}_{2}, \Delta_{2},\left\{\mathrm{~S}_{0}{ }^{\prime}\right\}, \mathrm{F}^{\prime}\right)$
- Product $\mathrm{P}=\left(\Sigma, \mathrm{S}_{1} \times \mathrm{S}_{2}, \Delta,\left\{\mathrm{~S}_{0}, \mathrm{~S}_{0}{ }^{\prime}\right\}, \mathrm{F}\right)$
- $\Delta\left(\left(\mathrm{s}_{1}, \mathrm{~S}_{2}\right), \sigma,\left(\mathrm{S}_{1}{ }^{\prime}, \mathrm{S}_{2}{ }^{\prime}\right)\right)=\Delta_{1}\left(\mathrm{~S}_{1}, \sigma, \mathrm{~S}_{1}{ }^{\prime}\right) \wedge \Delta_{2}\left(\mathrm{~S}_{2}, \sigma, \mathrm{~S}_{2}{ }^{\prime}\right)$
- $\left(s_{1}, s_{2}\right) \in F$ iff $s_{1} \neq \operatorname{err} \wedge s_{2} \in F^{\prime}$


## Example of Step 2

Property $\phi$ : F q

## Step 3: Checking if some state is visited infinitely often

- Suppose I show you the graph corresponding to the product automaton
- What graph property corresponds to "visited infinitely often"?



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- Suppose I show you the graph corresponding to the product automaton
- What graph property corresponds to "visited infinitely often"?
- Checking for a cycle with an accepting state
- We also need to check that the accepting state is reachable from the initial state


## DFS + cycle detection

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- How can we modify DFS to do cycle detection?
- Find strongly connected components, and then check if there's one with an accepting state
[But: we don't have the graph with us to start with]
- Use DFS to find an accepting state s
- On finding one, explore its child nodes.
- If a child node is on the stack, or if s has a self loop, we're done [Easy to see why]
- Else, do a new DFS starting from s to see if you can reach it again [Why will this work? Any modifications to the basic DFS needed?]
- SPIN's "nested DFS" algorithm


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- Product computation defines "accepting" states of P based on those of B

4. Check if some "accepting" state of $P$ is visited infinitely often

- If so: we found a bug (What does a counterexample look like?)
- If not, no bug in M


## What if our property is not LTL?

- Let's say the property is specified directly as a Buchi automaton B
- Then, to check if the system A satisfies the property, we use the same algorithm as before:
- Compute complement of $B$ : call it $B^{\prime}$
- Compute sync. product of $A$ and $B$ '
- Check for loops involving "accepting" states
- IMP: Buchi automata are closed under complementation, union, intersection
- Nondeterministic Buchi automata are strictly more expressive than deterministic Buchi automata!


## Time/Space Complexity

- Size measured in terms of:
$-N_{A}$ - num of states in system automaton
- $N_{B}$ - num of states in property automaton (for complement of the property we want to prove)
$-N_{S}$ - num of bits to represent each state
$-N_{E}$ - num transitions in product automaton
- Total size $=\mathrm{N}=\left(\mathrm{N}_{\mathrm{A}} * \mathrm{~N}_{\mathrm{B}} * \mathrm{~N}_{\mathrm{S}}\right)+\mathrm{N}_{\mathrm{E}}$
- Checking G p properties w/ DFS
- Time: ? Space: ?
- Checking arbitrary (liveness) properties w/ nested DFS
- Time:? Space:?


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- Checking G p properties w/ DFS
- Time: $\mathrm{O}(\mathrm{N} * \mathrm{~L})$ [X] Space: $\mathrm{O}(\mathrm{N})\{\mathrm{L}$ - lookup time to check if state visited already\}
- Checking arbitrary (liveness) properties w/ nested DFS
- Time: O(N*L) [2X] Space: O(N)


## Optimizations

- Complexity is a function of $\mathrm{N}_{\mathrm{E}}+\mathrm{N}_{\mathrm{A}} * \mathrm{~N}_{\mathrm{B}} * \mathrm{~N}_{\mathrm{S}}$
- Natural strategy to reduce time/space is to reduce:
$-N_{E}, N_{A} \rightarrow$ Partial-order reduction, Abstraction (later lecture)
$-N_{B} \rightarrow$ not really needed, $N_{B}$ is usually small
$-\mathrm{N}_{\mathrm{S}} \rightarrow$ State compression techniques


## Partial Order Reduction

- Edges of automata correspond to "actions" taken by the automaton
- Assume that you label each edge with its corresponding action
- Idea: Some actions are independent of each other
- E.g. "internal actions" of systems composed asynchronously
- You can permute them without changing the end state reached
- Both interleavings yield same end state

An Example

$P_{2}$
Initial state: $x=y=0$
Starting in $\left(s_{0}, t_{0}\right)$, what are the possible executions?

## Some Sample Properties: Are they preserved by P-O Reduction?

- $F(g, 2)$
- $G(x, y)$


## Key point: The property matters in deciding dependencies!

Atomic propositions that appear in the temporal logic property are termed "relevant atomic propositions"

## Implementing P-O Reduction

- At each state s , some set of actions is enabled: enabled(s)
- Of this set, we want to explore only a subset ample(s) s.t.
- We explore a subset of states and transitions
- The property holds for the reduced system iff it holds for the full system
- Pick an arbitrary element of ample(s) and execute that action
- QN: How to compute ample(s)?


## Independence and Invisibility

- Important properties of actions $\mathrm{a}, \mathrm{b}$ : independence \& invisibility
- Independence
- Enabledness: Action a should not disable b, and vice-versa
- Commutativity: $\mathrm{a}(\mathrm{b}(\mathrm{s}))=\mathrm{b}(\mathrm{a}(\mathrm{s}))$
- Invisibility
- $a$ and $b$ should not affect the values of any 'relevant' atomic propositions in the LTL property


## Problem

- Computing ample(s) exactly is as hard as computing the reachable states of the system!
- One of the conditions defining ample(s): Along every path starting at s , an action a dependent on action $b$ in ample(s) cannot be executed before $b$
- See [Ch. 10, Clarke,Grumberg,Peled] for a proof


## Computing ample(s)

- Conservative heuristics to compute actions that are NOT in ample(s):
- ample(s) cannot have actions that are visible or dependent on other actions in enabled(s)

1. If the same variable appears in two actions, they are dependent
2. If two actions appear in the same process/module, they are dependent
3. If an action shares a variable with a relevant atomic proposition, then it is visible

## Summary of P-O Reduction

- Very effective for asynchronous systems
- SPIN uses it by default


## State Compression Techniques

- Lossless
- Collapse compaction
- Essential a state encoding method
- Lossy
- Hash compaction
- Replace state vector by its hash; if you visit a state with same hash as previously visited, then don't explore further
- Bit-state hashing
- Think of the hash as a memory address of a single bit that represents whether the state has/hasn't been visited
- SPIN uses multiple (2) hashes per state
- 500 MB of memory can store $2.10^{9}$ states with 2 hashes
- Are errors found this way still valid errors?
- Often even if a state is missed, its successors are reached

