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EE147/247A Final, Fall 2015

NO CALCULATORS, CELL PHONES, or other electronics allowed. Show your work.
Use proper units in all answers.

1. [13] True/False (circle one) 1pt each

- a. True / False Vapor HF release causes stiction between polysilicon devices.
- b. True / False Self-assembled monolayers like OTS and DDMS cause stiction between polysilicon devices.
- c. True / False XeF₂ causes stiction between polysilicon devices.
- d. True / False Water causes stiction between polysilicon devices.
- e. True / False CO₂ critical point drying is used as a sacrificial etchant in surface micromachining.
- f. True / False Evaporated thin films give good step coverage.
- g. True / False All LPCVD thin films are conformal.
- h. True / False The DRIE etching rate depends on the size of the features being etched.
- i. True / False The resistivity of silicon varies by many orders of magnitude as the fraction of phosphorous dopant atoms increases from 0 to 1%.
- j. True / False The stiffness of silicon varies by many orders of magnitude as the fraction of phosphorous dopant atoms increases from 0 to 1%.
- k. True / False In a silicon crystal, silicon atoms are just over 1nm apart.
- l. True / False The stiffness of polysilicon is roughly the same in all directions.
- m. True / False The stiffness of single crystal silicon is roughly the same in all directions.

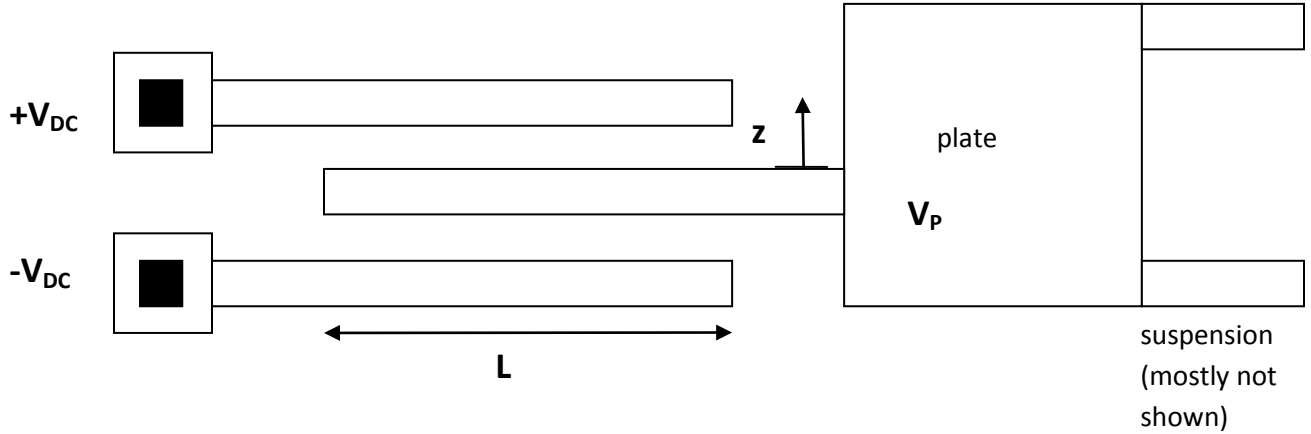
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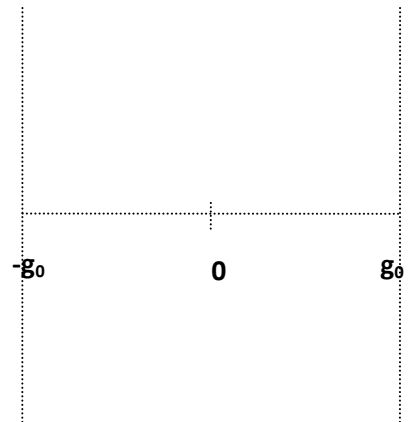
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2. [22] Short answer. Give a brief description in support of your answer (a sentence fragment is fine).
- a. How does the average thermal noise voltage on a capacitor change if the capacitance increases by a factor of 100?
 - b. How does the average thermal noise energy in a capacitor change if the capacitor increases by a factor of 100?
 - c. How does the spectral density (Watts/Hz) of the noise power in a resistor change if the resistance increases by a factor of 100?
 - d. How does the spectral density ($V/\sqrt{\text{Hz}}$) of the noise voltage on a resistor change if the resistance increases by a factor of 100?
 - e. What is the silicon etchant in the Bosch DRIE process?
 - f. Is the silicon etchant in the Bosch DRIE process isotropic or anisotropic?
 - g. Why is the Bosch DRIE process anisotropic?
 - h. In a gap-closing electrostatic actuator with a linear spring support, you can set the position of the actuator to one half of the gap by careful choice of applied voltage.
 - i. In a sputtering system, there's a capacitor between the AC supply and one electrode. Why?
 - j. In an RIE system, the substrate has a negative bias. Why?
 - k. In most electrostatic gap closing actuators, the voltage needed to close the gap is much larger than the voltage at which the gap will re-open. Why?

3. [16] You have a force-balanced accelerometer consisting of a suspended plate (mass), and comb fingers for both sensing and force feedback. A simplified diagram is shown below (the full suspension is not shown). There are two fixed fingers biased at plus and minus V_{DC} (equal magnitude, opposite sign). The finger overlap is L , the film thickness is t . The plate bias is V_p . Ignoring the sensing component, solve for the force on the plate in the z direction. At rest with $z=0$, the two gaps are both g_0 .



- Write an expression for the force on the plate in the z direction as a function of the plate voltage V_p , and the plate position z .
- When $z=0$, calculate the force on the plate (hint: it is linear in V_p !).
- When $V_p=0$, sketch the electrostatic force on the plate as a function of position for $z=[-g_0, g_0]$



- What is the electrostatic spring constant when $z=0$ and $V_p=0$?

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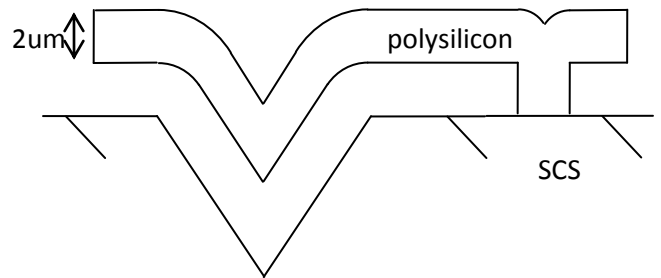
4. [16] A silicon dioxide cantilever beam of length L , width w , and thickness t has a small polysilicon resistor R patterned on its top surface at the base. You can assume that the polysilicon is thin enough that it does not affect the stress distribution in the cross-section when the beam deflects. The resistor is the only varying element in a Wheatstone bridge with an excitation voltage of V_x . The beam has a spring constant K , damping coefficient b , and a mass m .
- What is the axial strain ϵ in the polysilicon resistor as a function of a force F applied at the tip of the beam?
 - What is the change in resistance in the resistor as a function of the strain ϵ ?
 - What is the change in the bridge output voltage as a function of resistance change?
 - What is the noise force on the beam due to thermal noise?
 - What is the noise voltage in the resistor due to thermal noise?
 - What is the total noise displacement of the spring?
 - Assuming that the resistor noise dominates the total output noise (typically a good assumption) and $R=1k\Omega$, $V_x=1V$, what is the output noise in a 10 kHz bandwidth?
 - You apply a 1mN force to the end of the beam and see a 1V signal. What is the minimum detectable signal (noise equivalent force) in a 10 kHz bandwidth with $R=1k\Omega$, $V_x=1V$ as above?

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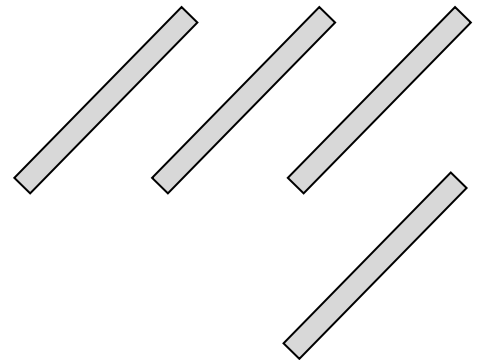
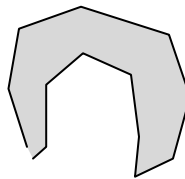
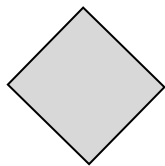
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5. [4] Write an equation that would let you calculate the angle between the (110) plane and the (100) plane in a silicon crystal (you don't need to solve the inverse trig problem, but simplify the easy parts)

6. [10] Write down a process flow that would let me make the following cross-section.



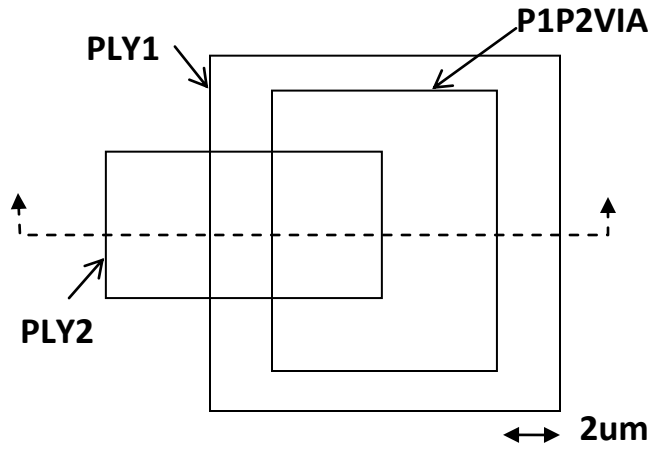
7. [6] A (100) wafer coated with silicon dioxide has the following regions opened to the silicon surface. The wafer is dropped in a KOH etch and the etch runs until only {111} planes are exposed. What is the outline of the etched regions under the silicon dioxide (i.e. where is the region where the SiO₂ will not be supported by silicon)? Assume that this page is oriented with the wafer flat.



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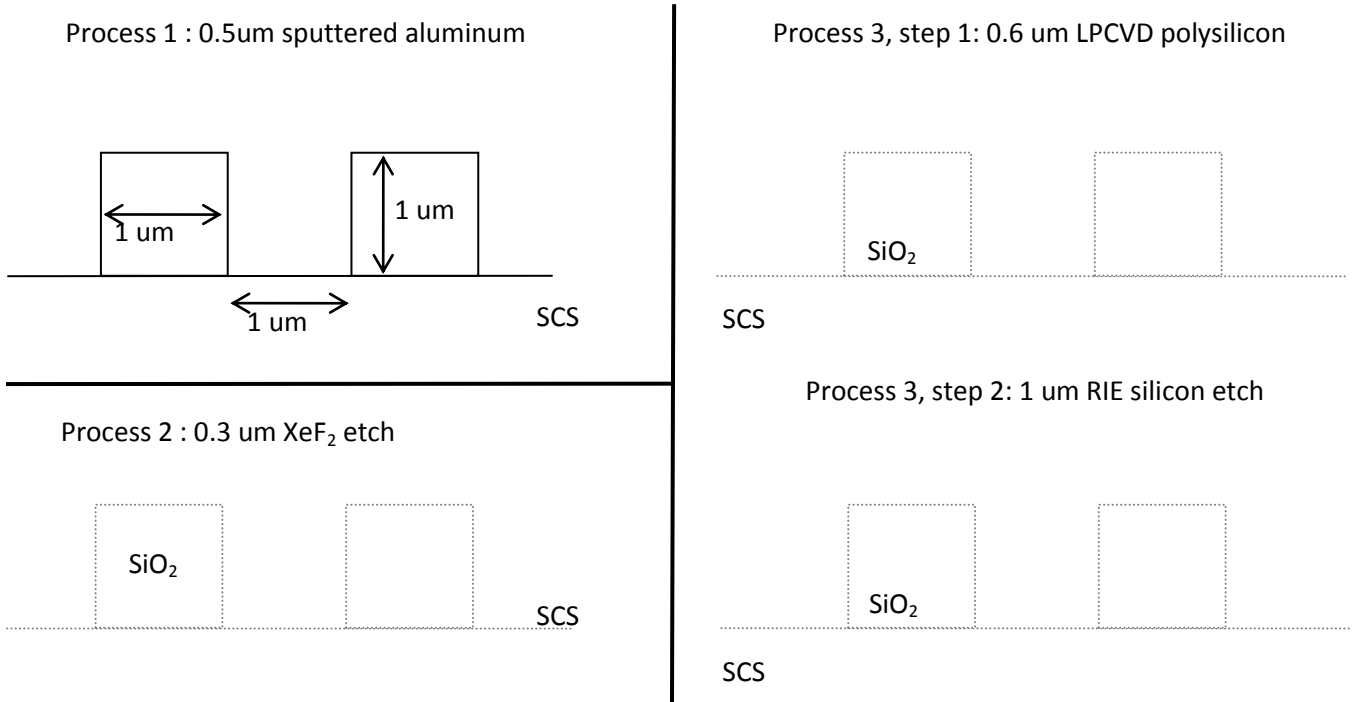
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11. [10 total] In the polyMUMPS process you draw the following layout.



Draw a cross-section of the resulting device assuming infinite selectivity in all etches, 20% overetch in all etches except PLY2, which has 200%.

12. [12] In the following figures, assume that an SiO₂ film 0.5um thick has been patterned with a 1um line and space on two different silicon wafers. The cross section of two lines is shown below. Draw the expected cross-section for three different processes: 1) 0.5um sputtered aluminum; 2) 0.3um etch in XeF₂; 3) 0.6um LPCVD polysilicon followed by a 1 um silicon RIE. RIE selectivity to oxide is 2:1. The substrate is single-crystal silicon. For process 2, draw the cross-section after each of the two steps.



13. [6] In the following figures, a 1 micron thick SiO₂ layer has been patterned to expose bare [100] silicon. Both apertures are 10um square, one is oriented in the 110 direction (parallel to the wafer flat), one is oriented in the 100 direction (45 degrees from the wafer flat). Draw the cross section after a 3 um etch in KOH.

