

EE 147/247A Linear/Analog ICs

extra homework problems
more project requirements

Admin

What is analog?

What will we study/learn?

Who am I?

Grading

HW+Labs 25

Midterms 20

Project 25

Final 30

Labs a few 50-minute labs in the first 6 weeks

HW Due Monday, Late Tues 9AM.

Self-study. ~~Resubmission~~ Resubmission/requests OK within 1 week

Lecture mwf 1-2

Dis F 10-11, 2-3

Labs m 5-8p
th 2-5p

W 8-11

X

OH

M 9:30-10:30
W 2-3

TA David Borrett

What is analog?

- anything not digital

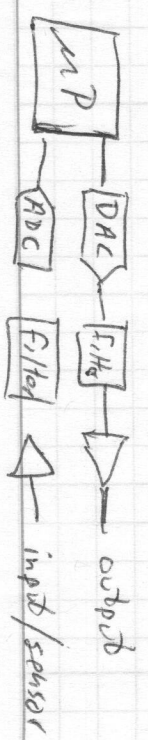
- lots of what gets called digital
SRAM, DRAM, flash

digital communicators - anything high performance
- ethernet, HDMI, USB, RF

- interfaces to the real world, e.g. cell phone

inputs: mphones, accel, gyro, magnetometer
cameras, fingerprint sensors

outputs: speakers, buzzer, LEDs, displays, backlight



293 this week

125 copy



next week



X

X



And all of the stuff that you need to make that work: battery management (charging, shutdown)

supply regulation

voltage reference

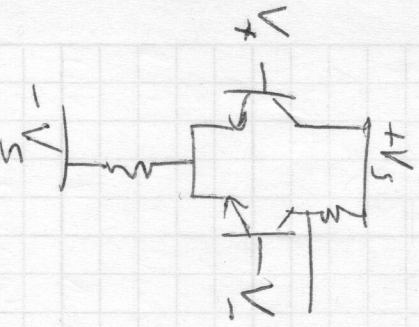
temp sensor

power on reset

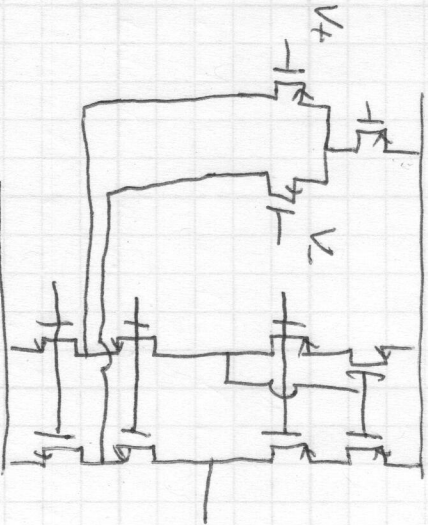
find project will design many of these components in a real process

Real op-amps

have minimum



better



most of these rely on op-amps



Ideal op-amp

gain	∞
BV	∞
input impedance	∞
CMRR	∞
PSRR	∞
input offset	0
stability	uncond.

Are these "ideal"? No way!

Are they good enough? How do you know?

→ Hand analysis

gain, BV , power

frequency response, feedback, stability

process, voltage, temp (PVT) variation response

→ SPICE simulation

- gets the details right

We will use Cadence for schematic design & SPICE + layout/extraction

Same software used in industry

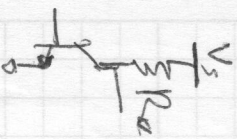
Who ~~was~~ designs analog circuits?

- every cell phone company Apple, Samsung, ...
- every microprocessor company Intel, Broadcom, Qualcomm, ...
- every communication company Cisco, Marvell

- ADI, TI, Freescale, NXP, Linear Tech, ST, ...

~ \$50B industry

How to solve?



$$V_{out} = V_S - I_D R_D$$

approximate $e^{V_{GS}/V_T} - 1 \approx e^{V_{GS}/V_T}$

$$1 + \frac{V_{GS}}{V_A} \approx 1$$

$$V_{out} = V_S - I_D R_D \quad \text{translated}$$

What did we learn in eel05?

Regions of Operation

Large signal behavior of BJT, MOS devices

$$I_C = I_S \left(e^{\frac{V_{BE}}{V_T}} - 1 \right) \left(1 + \frac{V_{CE}}{V_{AN}} \right) \quad (6.55)$$

$$I_D = \begin{cases} 0 & V_{GS} < V_{TH} \\ \frac{\mu_n C_{ox}}{2} \frac{W}{L} (V_{GS} - V_{TH})^2 \left(1 + \lambda V_{DS} \right) & V_{GS} \geq V_{TH} \end{cases} \quad (1.165)$$

