

A JVM for the Barrelfish Operating System

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Introduction

- ▶ Future multi-core architectures will presumably...
 - ▶ ...have a larger numbers of cores
 - ▶ ...exhibit a higher degree of diversity
 - ▶ ...be increasingly heterogenous
 - ▶ ...have no cache-coherence/shared memory
- ▶ These changes (arguably) require new approaches for Operating Systems: e.g. *Barrelfish*, *fos*, *Tessellation*,...
- ▶ Barrelfish's approach: treat the machine's cores as nodes in a distributed system, communicating via message-passing.
- ▶ **But:** How to program such a system uniformly?
- ▶ How to exploit performance on all configurations?
- ▶ How to structure executables for these systems?

Introduction

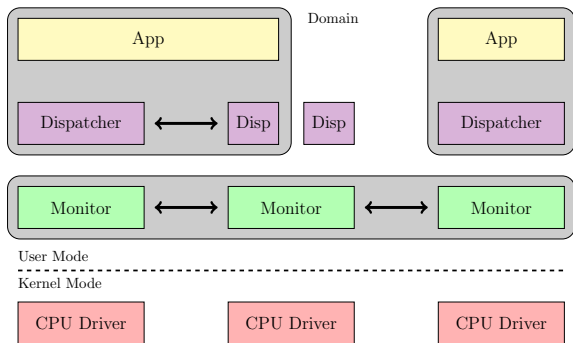
- ▶ **Answer:** Managed Language Runtime Environments (e.g. *Java Virtual Machine, Common Language Runtime*)
- ▶ Advantages over a native programming environment:
 - ▶ Single-system image
 - ▶ Transparent migration of threads
 - ▶ Dynamic optimisation and compilation
 - ▶ Language extensibility
- ▶ Investigate challenges of bringing up a JVM on Barrelfish.
- ▶ Comparing two different approaches:
 - ▶ Conventional shared-memory approach
 - ▶ Distributed approach in the style of Barrelfish

Outline

1. The Barrelfish Operating System
2. Implementation Strategy
 - ▶ Shared-memory approach
 - ▶ Distributed approach
3. Performance Evaluation
4. Discussion & Conclusions
5. Future Work

The Barrelfish Operating System

- ▶ Barrelfish is based on the Multikernel Model: Treats multi-core machine as a distributed system.
- ▶ Communication through a lightweight message-passing library.
- ▶ Global state is replicated rather than shared.

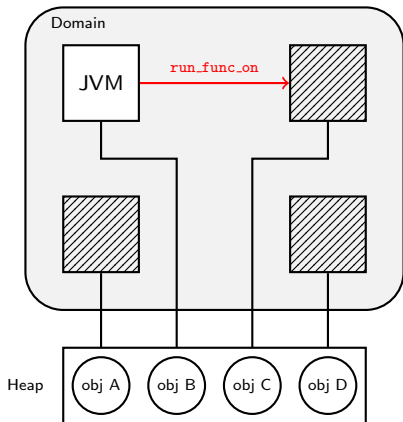


Implementation

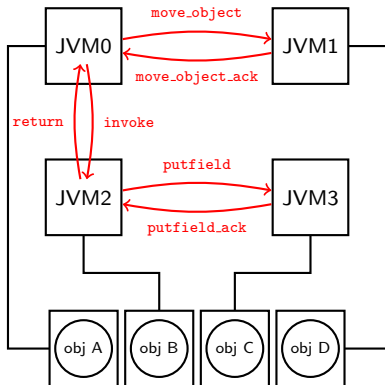
- ▶ Running real-world Java applications would require bringing up a full JVM (e.g. the *Jikes RVM*) on Barrelfish.
- ▶ Stresses the memory system (virtual memory is fully managed by the JVM), Barrelfish lacked necessary features (e.g. page fault handling, file system).
- ▶ Would have distracted from understanding the core challenges.
- ▶ **Approach:** Implementation of a rudimentary Java Bytecode interpreter that provides just enough functionality to run standard Java benchmarks (*Java Grande Benchmark Suite*).
- ▶ Supports 198 out of 201 Bytecode instructions (except `wide`, `goto_w` and `jsr_w`), Inheritance, Strings, Arrays, Threads,...
- ▶ No Garbage Collection, JIT, Exception Handling, Dynamic Linking or Class Loading, Reflection,...

Shared memory vs. Distributed approach

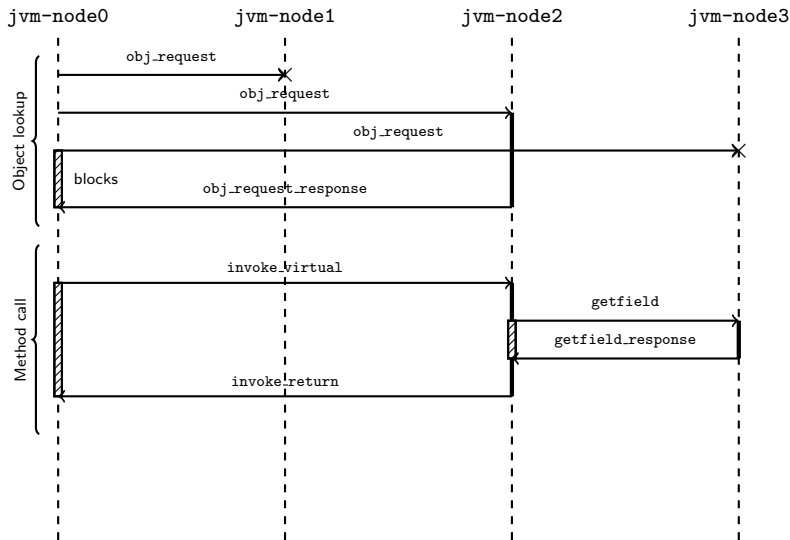
Shared memory



Distributed Approach



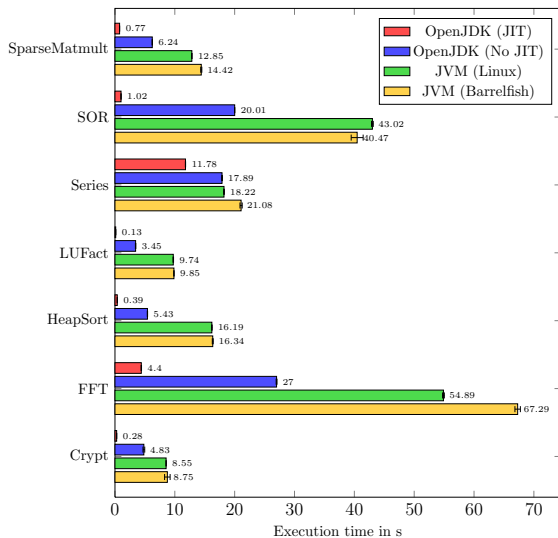
The distributed approach



Performance Evaluation

- ▶ Performance evaluation using the sequential and parallel *Java Grande Benchmarks* (mostly Section 2 - compute kernels).
- ▶ Performed on a 48-core AMD Magny- Cours (Opteron 6168).
- ▶ Four 2x6-core processors, 8 NUMA nodes (8GB RAM each).
- ▶ Evaluation of the shared-memory version on Linux (using `numactl` to pin cores) and Barrelfish.
- ▶ Evaluation of the distributed version only on Barrelfish.
- ▶ Compared performance to industry-standard JVM (OpenJDK 1.6.0) with and without JIT compilation.

Sequential Performance

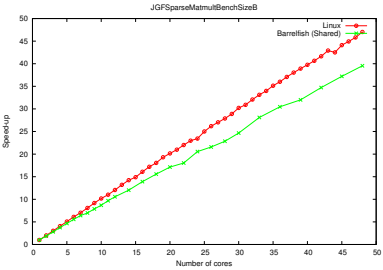
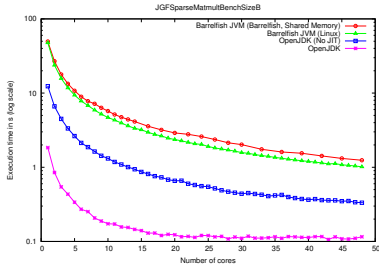


Performance of the shared-memory approach

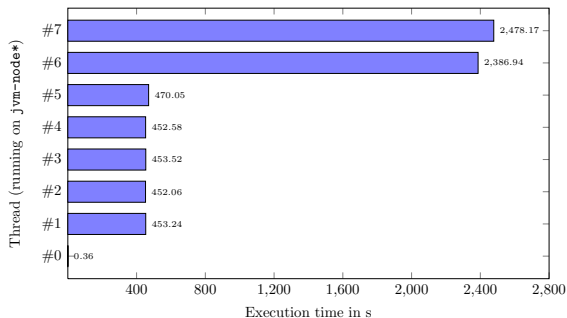
- ▶ Using the parallel sparse matrix multiplication Java Grande benchmark JGFSparseMatmultBenchSizeB

Raw Performance

Speed-up



Performance of the distributed approach



Cores	Run-time in s	σ (Standard deviation)
1	2.70	0.002
2	458	7.891
3	396	3.545
4	402	7.616
5	444	2.128
6	514	36.77
7	1764	247.7
8	2631	335.9
16	9334	(only executed once)

Discussion

- ▶ Performance of shared-memory approach is similar on Linux and Barrelfish (overhead arguably from agreement protocols).
- ▶ Distributed approach is orders of magnitude slower. Overhead caused by inter-core communication (150-600 cycles) and message handling in Barrelfish.
- ▶ For this benchmark, have to exchange 7 pairs of messages for each iteration of the kernel, while shared-memory approach requires almost no inter-core communication.
- ▶ How can these overheads be alleviated?
 - ▶ Caching of objects and arrays (reduce communication).
 - ▶ Hardware support for message-passing (e.g. Intel SCC).

Conclusion & Future Work

- ▶ Preliminary results show that future work should focus on reducing message-passing overhead and number of messages.
- ▶ Promising future work for the JVM:
 - ▶ A caching protocol for arrays, similar to a directory-based MSI cache coherence protocol.
 - ▶ Running the Barrelfish JVM on the Intel SCC.
- ▶ Additional areas of interest:
 - ▶ Garbage Collection on such a system.
 - ▶ Relocation of objects at run-time.
- ▶ Future work should investigate bringing up the Jikes RVM on Barrelfish, focussing on these aspects.

Questions?