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## Microelectromechanical Components for Miniaturized Low-Power Communications

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#### ABSTRACT

An overview of recent progress in the research and development of microelectromechanical devices for use in wireless communication sub-systems is presented. Among the specific devices described are tunable micromachined capacitors, integrated high-Q inductors, and micro-scale vibrating mechanical resonators with Q's in the tens of thousands. Specific applications are reviewed for each of these components with emphasis on methods for miniaturization and performance enhancement of existing and future wireless transceivers.

## **1. INTRODUCTION**

Vibrating mechanical tank components, such as crystal and SAW resonators, are widely used for frequency selection in communication sub-systems because of their high quality factor (Q's in the tens of thousands) and exceptional stability against thermal variations and aging. In particular, the majority of heterodyning communication transceivers rely heavily upon the high Q of SAW and bulk acoustic mechanical resonators to achieve adequate frequency selection in their RF and IF filtering stages and to realize the required low phase noise and stability in their local oscillators. In addition, discrete inductors and variable capacitors are used to properly tune and couple the front end sense and power amplifiers, and to implement widely tunable voltage-controlled oscillators. At present, the aforementioned resonators and discrete elements are off-chip components, and so must interface with integrated electronics at the board level, often consuming a sizable portion of the total sub-system area. In this respect, these devices pose an important bottleneck against the ultimate miniaturization and portability of wireless transceivers. For this reason, many research efforts have been focused upon strategies for either miniaturizing these components [1-5] or eliminating the need for them altogether [6-8].

The rapid growth of IC-compatible micromachining technologies that yield micro-scale, high-*Q* tank components may now bring the first of the above strategies closer to reality. Specifically, the high-*Q* RF and IF filters, oscillators, and couplers, currently implemented via off-chip resonators and discrete passives may now potentially be realized on the micro-scale using micromachined equivalents based on a variety of novel devices, including high-*Q* on-chip vibrating mechanical resonators [10-12], voltage-tunable on-chip capacitors [13], isolated low-loss inductors [14-18], microwave/mm-wave medium-*Q* filters [19-22], structures for high frequency isolation packaging [23-24], and low loss micromechanical switches [25-27]. Once these miniaturized filters and oscillators become available, the fundamental bases upon which communication systems are developed may also evolve, giving rise to new system architectures with possible power and bandwidth efficiency advantages. For systems operating past X-Band, antennas can also be micromachined with potential cost savings and with additional capabilities attained via active antenna arrays (e.g., phased arrays, power combining, etc.) [28-30].

This abstract provides a prelude to the presentation material that follows. It begins with a brief introduction into the needs of wireless communication transceivers, identifying specific functions that could greatly benefit from micromechanical implementation, and describing methods for substantially reducing power consumption by using micromechanical devices in alternative transceiver architectures. The presentation material that follows then reviews several specific devices, with particular emphasis on frequency-selective microelectromechanical components for high-*Q* oscillators and filters. It concludes with suggestions on how this micro-scale technology can best be used to revolutionize wireless communications.

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Fig. 1: System-level schematic detailing the front-end design for a typical wireless transceiver. The off-chip, high-Q, passive components targeted for replacement via micromechanical versions (suggestions in lighter ink) are indicated in the figure.

#### 2. ADVANTAGES OF MEMS IN COMMUNICATION TRANSCEIVERS

To illustrate more concretely the specific transceiver functions that benefit from micromechanical implementation, Fig. 1 presents the system-level schematic for the front-end of a typical super-heterodyne wireless transceiver. As implied in the figure, several of the constituent components can already be miniaturized using integrated circuit transistor technologies. These include the low noise amplifiers (LNA's) in the receive path, the solid-state power amplifier (SSPA) in the transmit path, synthesizer phase-locked loop (PLL) electronics, mixers, and lower frequency digital circuits for baseband signal demodulation. Due to noise, power, and frequency considerations, the SSPA (and sometimes the LNA's) are often implemented using compound semiconductor technologies (i.e., GaAs). Thus, they often occupy their own chips, separate from the other mentioned transistor-based components, which are normally realized using silicon-based bipolar and CMOS technologies. However, given the rate of improvement of silicon technologies (silicon-germanium included [31]), it is not implausible that all of the above functions could be integrated onto a single-chip in the foreseeable future.

Unfortunately, placing all of the above functions onto a single chip does very little towards decreasing the overall super-heterodyne transceiver size, which is dominated not by transistor-based components, but by the numerous passive components indicated in Fig. 1. The presence of so many frequency-selective passive components is easily justified when considering that communication systems designed to service large numbers of users require numerous communication channels, which in many implementations (e.g., Time Division Multiple Access (TDMA)) must have small bandwidths and must be separable by transceiver devices used by the system. The requirement for small channel bandwidths results in a requirement for extremely selective filtering devices for channel selection and extremely stable (noise free) local oscillators for frequency translation. For the vast majority of cellular and cordless standards, the required selectivity and stability can only be achieved using high-*Q* components, such as discrete inductors, discrete tunable capacitors (i.e., varactors), and SAW and quartz crystal resonators, all of which interface with IC components at the board level. The needed performance cannot be achieved using conventional IC technologies, because such technologies lack the required *Q*. It is for this reason that virtually all commercially available cellular or cordless phones contain numerous passive SAW and crystal components.

The presentation that follows describes methods for reducing the size and power consumption of portable transceivers by first replacing high-*Q* passives by micromechanical versions, then extending their system-level presence by using them in large quantities. Among the components targeted for replacement in cellular and cordless applications are RF filters, including image reject filters, with center frequencies ranging from 800 MHz to 2.5 GHz; IF filters, with center frequencies ranging from

455 kHz to 254 MHz; high-*Q*, tunable, low phase noise oscillators, with frequency requirements in the 10 MHz to 2.5 GHz range; and switches for transmit/receive (T/R) selection, antenna selection, and multi-band configurability.

### 2.1. Miniaturization and IC-Compatibility

Reduced size constitutes the most obvious incentive for replacing SAWs, crystals, and other discrete passives by equivalent µmechanical devices. The substantial size difference between micromechanical components and their macroscopic counterparts is illustrated in Fig. 3, which compares a typical SAW resonator with a clamped-clamped beam micromechanical resonator of comparable frequency. The particular µresonator shown is excited electrostatically via parallelplate capacitive transducers and designed to vibrate in a direction parallel to the substrate with a frequency determined by material properties, geometric dimensions, and stress in the material. Typical dimensions for a 100 MHz micromechanical resonator are  $L \approx 12.9 \,\mu\text{m}$ ,  $W = 2 \,\mu\text{m}$ , and h = 2µm. With electrodes and anchors, this device occupies an area of 420  $\mu$ m<sup>2</sup> = 0.00042 mm<sup>2</sup>. Compared with the several mm<sup>2</sup> required for a typical VHF range SAW resonator, this represents several orders of magnitude in size reduction.

A related incentive for the use of micromechanics is integrability. Micromechanical structures can be fabricated using





the same planar process technologies used to manufacture integrated circuits. Several technologies demonstrating the merging of CMOS with surface micromachining have emerged in recent years [10,32-34], and one of these is now used for high volume production of commercial accelerometers [32]. Using similar technologies, complete systems containing integrated micromechanical filters and oscillator tanks, as well as amplification and frequency translation electronics, all on a single chip, are possible. This in turn makes possible high-performance, single-chip transceivers, with super-heterodyne architectures and all the communication link advantages associated with them. Other advantages inherent with integration are also obtained, such as elimination of board-level parasitics that could otherwise limit filter rejections and distort their passbands.

## 2.2. Power Savings Via MEMS

Although certainly a significant advancement, miniaturization of transceivers only touches the surface of the true potential of this technology. MEMS technology may in fact make its most important impact not at the component level, but at the system level, by offering alternative transceiver architectures that emphasize selectivity over complexity to substantially reduce power consumption and enhance performance.

The power savings advantages afforded by MEMS is perhaps best illustrated by comparison with recent attempts to reduce the cost and size of wireless transceivers via increased circuit complexity. Specifically, in these approaches higher levels of transistor integration and alternative architectures are used to reduce the need for the off-chip, high-Q passives used in present-day super-heterodyne transceivers, with obvious size advantages. Unfortunately, removal of off-chip passives often comes at the cost of increased power consumption in circuits preceding and



Fig. 4. Modified signal flow diagrams for a conventional receiver using wideband RF filters.

including the analog-to-digital converter (ADC), which now must have higher dynamic ranges to avoid desensitization caused

by larger adjacent channel interferers. A selectivity (or Q) versus power trade-off is clearly seen here.

To better convey this point, specific phenomena that give rise to receiver desensitization are illustrated in the diagram of Fig. 4, which depicts the signal flow for a desired signal at  $\omega_{inf}$  with two adjacent interferers (offset  $\Delta \omega$  and  $2\Delta \omega$ ) from antenna to baseband in a conventional receiver architecture using wideband RF filters. As shown, due to nonlinearity in the low-noise amplifier (LNA) and phase noise in the local oscillator, the presence of interferers can potentially desensitize the receiver by (1) generating third-order intermodulation  $(IM_3)$  distortion components over the desired signal at the output of the LNA; and (2) aliasing superposed phase noise sidebands from the local oscillator onto the desired signal immediately after the mixer stage. In order to avoid such desensitization, the LNA must satisfy a strict linearity requirement, and the local oscillator a strict phase noise requirement, both of which demand significantly higher power consumption in these components. Similar increases in power consumption are also often necessary to maintain adequate dynamic range in subsequent stages (e.g., the A/D converter).

A method for eliminating such a waste of power becomes apparent upon the recognition that the above desensitization phenomena arise in conventional architectures only because such architectures allow interfering signals to pass through the RF filter and reach the LNA and mixer. If these signals were instead eliminated at the outset by a much more selective RF filter, then interference from  $IM_3$  components and from phase noise sidebands would be greatly alleviated, as shown in Fig. 5, and specifications on linearity and phase noise could be greatly relaxed. The power savings afforded by such relaxations in specifications is potentially enormous, especially when considering the possibility of replacing conventional Class A or AB type amplifiers with more efficient topologies, such as Class E. The above discussion pertains to the receive path, but if channel-select filters with both sufficiently high Q and power handling capability are available and placed right



Fig. 5. Modified signal flow diagrams for an RF channel-select receiver.





before the transmitting antenna, similar power savings are possible for the *transmit* local oscillator and power amplifier, as well.

An architecture such as shown in Fig. 5 requires a tunable, highly selective (i.e., high-Q) filter capable of operation at RF frequencies. Unfortunately, partially due to their own high stability, high-Q filters are generally very difficult to tune over large frequency ranges, and MEMS-based filters are no exception to this. Although µmechanical resonators can be tuned over larger frequency ranges than other high-Q tank technologies, with voltage-controllable tuning ranges of up to 5% depending on design, a single micromechanical filter still lacks the tuning range needed for some wide-band applications

Thanks to the tiny size of micromechanical filters, however, there no longer needs to be only one filter. One of the major advantages of micromechanical filters is that, because of their tiny size and zero dc power dissipation, many of them (perhaps

hundreds or thousands) can be fabricated onto a smaller area than occupied by a single one of today's macroscopic filters. Thus, rather than use a single tunable filter to select one of several channels over a large frequency range, a massively parallel bank of switchable micromechanical filters can be utilized, in which desired frequency bands can be switched in, as needed. The simplified block diagram for such a front-end architecture is illustrated in Fig. 6, where each filter switch combination corresponds to a single micromechanical filter, with input and output switches activated by the mere application or removal of dc-bias voltages from the resonator elements [11]. By further exploiting the switching flexibility of such a system, some very resilient frequency-hopping spread spectrum transceiver architectures can be envisioned that take advantage of simultaneous switching of high-*Q* micromechanical filters and oscillators.

In effect, frequency-selective devices based on MEMS technologies can potentially enable substantial power savings by making possible paradigm-shifting transceiver architectures that, rather than eliminate high-Q passive components, attempt to maximize their role with the intention of harnessing the Q versus power trade-off often seen in transceiver design. The next sections now focus upon the subject micromechanical resonator devices.

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Presentation material now follows.

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**Anchor Dissipation in Clamped-Clamped Beams** 0 8.5 MHz -5 10 -10 -20 -5 *Q*=8,000 -20 which with -25 8.48 8.49 8.50 8.51 8.52 8.53 Frequency [MHz] *L<sub>r</sub>*=40.8µm, *W<sub>r</sub>*=8µm, *h*=2µm, d=1,000Å, W<sub>e</sub>=20µm, V<sub>e</sub>=35V Anchor Electrodes

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