Nickel Vibrating Micromechanical Disk Resonator
With Solid Dielectric Capacitive-Transducer Gap

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Abstract—A MEMS-based vibrating disk resonator fabricated in a low temperature nickel metal material and using a 30-nm nitride dielectric capacitive transducer has been demonstrated at frequencies approaching 60 MHz with $Q$'s as high as 54,507, which is the highest to date for any micro-scale metal resonator in the VHF range. The frequency-$Q$ product of $3.3 	imes 10^{12}$ achieved by this device is three orders of magnitude higher than the $1.1 	imes 10^9$ of previous nickel micromechanical resonators. The degree of isolation afforded by its supports strongly governs the achievable $Q$'s of this device, which vary from 490 when a 2 $\mu$m-radius supporting stem is used, to 54,507 when no stem is used, clearly indicating an anchor dominated loss mechanism, but more importantly indicating that nickel's intrinsic material $Q$ is quite high at VHF. Furthermore, because its highest process temperature is 380°C, and there are paths to an even lower temperature ceiling (e.g., 100°C), the fabrication process for nickel disks is amenable to post-processing over finished foundry CMOS wafers, even those using advanced low-k dielectrics around copper metallization. Nickel material thus presents an intriguing path towards a complete communication transceiver (including all high $Q$ passives) on a chip.

Keywords—MEMS, micromechanical resonator, nickel, capacitive transducer, quality factor, charge bias, integration.

I. INTRODUCTION

The advent of vibrating micromechanical resonators operating at GHz frequencies with $Q$'s $>10,000$ [1][2][3] has generated great interest in RF front-end architectures that dispense with previous restrictions on the number of high $Q$ passives used, and rather attempt to utilize them in massive quantities in order to achieve enhanced robustness, lower power consumption, and greater multi-mode reconfigurability [4]. Among the more aggressive of the proposed architectures that fully harness the capabilities of vibrating RF MEMS is one based on a front-end RF channel-selector, where micromechanical filters [5] placed in large banks directly after the antenna can be switched on and off to select the desired channel(s) directly at RF. Hundreds, perhaps thousands, of filters might be required to cover all needed bands in a practical implementation, but if MEMS technology can truly realize the dense integrated micromechanical circuits predicted and described in [6], then such numbers would not be problematic.

Interconnecting the many elements of such an integrated micromechanical circuit to transistor electronic circuits, however, poses a challenging prospect. In particular, the number and density of interconnections to the many micromechanical filters might make a bond-wire approach impractical, or uneconomical. When the number of required MEMS-to-transistor interconnects exceeds 1000, direct monolithic integration of MEMS and transistors onto a single die becomes a much more attractive approach to interconnection.

To date, several approaches to monolithic integration of MEMS and transistors have been developed and implemented over the past decade. The most prevalent methods in production [7] presently utilize process flows that intermix and share steps from the original stand-alone MEMS and transistor process flows, seeking to reduce the total number of steps required in a given merging process. Unfortunately, however, such an intermixed process often precludes advancement in the base MEMS or transistor technologies used, since altering one of the base processes would require a re-design of the whole integration process. It is for this reason that many products based on intermixed integration processes still use 1980's transistor circuit technology; i.e., switching to a more modern transistor technology (e.g., with 65 nm channel lengths) would be too costly.

To allow process advancement in next generation products, more modular approaches to integration have been developed that separate the base MEMS and transistor process flows into modules and run one module before the other, with no intermixing of steps. In such a process, a change in one module could ideally be made without affecting the other module or the method of modular merging. Advancements in a particular module could be incorporated without a redesign of the whole process. To date, several modular MEMS-transistor merging processes have been demonstrated, some placing the MEMS module before the transistor one [8][9], others vice versa [10][11][12]. Of these processes, the ones based on post-transistor MEMS are perhaps most amenable to integration with foundry CMOS. In particular, pre-transistor MEMS processes require that IC foundries accept wafers that have been pre-processed, and so far, IC foundries have been reluctant to do this.

The MICS process [11][12], summarized very briefly in Fig. 1, integrates MEMS directly over CMOS in a fully
planar process. Originally done using polysilicon MEMS, the latest rendition of MICS, shown in Fig. 2, uses poly-SiGe MEMS above 0.18 μm CMOS and is presently among the most successful pre-transistor processes available. The key to this process is the ability to deposit poly-SiGe MEMS structural material at temperatures below 450°C, which can be withstood by copper metallization and associated dielectrics used in 0.18 μm CMOS processes. Next generation CMOS processes, however, from 65 nm on down, are beginning to use low-k dielectric materials over their metals that may no longer survive temperatures over 400°C. To accommodate this lower temperature ceiling, new structural materials are needed for vibrating RF MEMS applications that can be deposited at very low temperatures while still retaining high Q. It is possible that a temperature ceiling as low as 200°C might eventually be needed as transistor technology continues to advance.

Pursuant to finding such a material, this work investigates nickel as a potential high frequency, high Q structural material for RF applications. In particular, a MEMS-based vibrating disk resonator fabricated in a low temperature nickel metal material and using a 30 nm nitride dielectric capacitive transducer has been demonstrated at frequencies around 60 MHz with Q’s as high as 54,507, which is the highest to date for any micro-scale metal resonator in the VHF range. The frequency-Q product of 3.3×10^{12} achieved by this device is three orders of magnitude higher than the 1.1×10^{9} of previous nickel micromechanical resonators. Because its highest process temperature is 380°C, and there are paths to an even lower temperature ceiling (e.g., 100°C), the fabrication process for this nickel disk is amenable to post-processing over finished foundry CMOS wafers, even those using advanced low-k dielectrics around copper metallization. The nickel material of this work thus presents a potential path towards a complete communication transceiver (including all high Q passives) on a chip.

II. INTRINSIC LOSS OR ANCHOR LOSS?

With an electroplating deposition temperature of only 50°C, nickel was recognized early as a potential structural material for post-transistor high Q vibrating RF MEMS devices. Most notable among efforts to use nickel as a resonator structural material is the ring gyroscopes by Putty and Najafi [10] in 1994, which not only achieved a functional gyroscope, but also integrated the micromechanical resonating proof mass element together with active CMOS electronics, all in a single planar process that realized the MEMS in post-transistor fashion. Although the gyroscope was functional, the Q of its 33.5-kHz plated-nickel proof mass resonator was only 2,400, and its frequency drifted at a rate of 14ppm/day. These problems compelled researchers to abandon nickel as the structural material and replace it with single-crystal silicon, which was more stable and capable of much higher Q’s. With a resolution 7.2°/hr, the single-crystal silicon vibratory ring gyroscope of [13] is still among the lowest resolution MEMS-based gyroscopes implemented to date.

Despite the Q and drift problems encountered in early attempts to use nickel structural material in MEMS devices, efforts to improve the material continued, spurred by its great potential for post-transistor planar integration. In 1999, work by Hsu and Nguyen [14] introduced a method dubbed “in situ localized annealing”, where an electrical current is sent through the conductive mechanical structure of a resonator device to heat it to temperatures as high as 886°C while vibrating the device at its resonance frequency. Application of this method to 75-kHz folded-beam resonators was found to raise their Q’s from 4,103 to 14,172, and to greatly improve their drift stability. In principle, operation of the resonator device at high temperature is thought to redistribute or remove defects and contaminants from its volume and surfaces, effectively removing intrinsic loss mechanisms, and thereby allowing much higher Q and stability.

Later attempts in 1999 to apply localized annealing to higher frequency clamped-clamped beam (“CC-beam”) and free-free beam (“FF-beam”) resonators made in nickel structural material, however, were not successful. In par-
ticular, only CC-beam devices were functional in the only fabrication run devoted to this evaluation, and these posted $Q$'s on the order of only 532. The FF-beam devices, which were much better isolated from their anchors, were unfortunately not functional. Thus, at the time this work was discontinued shortly after 2000, there was no conclusion on whether the low $Q$'s of VHF CC-beam nickel resonators were caused by anchor loss mechanisms, or intrinsic material loss mechanisms.

III. RESONATOR TYPES UNDER TEST

To quantify the influence of anchor losses on nickel resonator $Q$, this work focuses on the compound (2,1) mode disks, often dubbed “wine-glass disks”, first introduced in [15]. This wine-glass disk design is quite suitable for anchor loss evaluation, since, as shown in [15], the amount of anchor loss experienced by this particular design can be specified by selection of the anchor type (i.e., center stem or side support), the number of anchors, and the geometry of the anchors (i.e., stem size).

Figure 3 presents the perspective-view schematic of one of the wine-glass disk resonators used in this work, hooked up in its typical electrical bias, excitation, and measurement scheme. As seen in the figure, the device is composed of a 3 um-thick nickel disk resonator suspended 400 nm above a ground plane by a stem placed at the disk center and anchored to the substrate below. Four separate electrodes spaced less than 100 nm from the disk edges are placed on four different quadrants surrounding the disk to specifically excite its compound (2,1) mode, which is shown in Fig. 4. In practice, the gaps between the resonator and its electrodes can be either air-filled or solid-filled, with the latter the more preferable, since solid-filled gaps both reduce resonator impedance and raise the fabrication yield of devices.

To operate this device, a DC bias $V_p$ is applied to the disk structure and an AC input voltage $v_i$ is applied to one pair of opposing electrodes along the same axis (i.e., the input axis). (Note that no current flows once the structure is charged to $V_p$, so there is no dc power consumption.) These voltages then collectively create a time-varying electrostatic excitation force between the electrodes and the disk, in the lateral direction, and at the frequency of the AC input voltage if $v_i \approx v_i$. When the AC input frequency matches the disk resonance frequency, the resulting resonance electrostatic force drives the disk into resonance vibration, which then generates resonance motion along the orthogonal output axis, depicted in Fig. 3. Motion along the output axis results in a DC biased time-varying capacitance at the output electrodes, which produces an output current $i_o$ given by

$$i_o = V_o \left(\frac{\partial C}{\partial t}\right) \approx V_o \left(\frac{e_o A_o}{d_o^2}\right) \frac{\omega_o X}{k} \frac{\epsilon_o^2 \pi R H^2}{d_o} v_i$$

where $A_o = (\pi R^2)H$, $R$, $H$, $d_o$, $k_o$, and $Q$ are the electrode-to-resonator overlap area, radius, thickness, gap spacing, effective stiffness, and quality factor, respectively, of the disk resonator; $X$ is the amplitude of resonator vibration; $\omega_o = 2\pi f_o$ is the radian resonance frequency; $\epsilon_o$ is the dielectric constant; and an approximate form for $\partial C/\partial t$ has been used.

The expression for resonance frequency has been presented previously in [16], but is repeated here for convenience, as

$$\Psi'\left(\frac{\zeta}{2} - q\right) \Psi'\left(\frac{\zeta}{2} - q\right) = (2q - 2)^2$$

where

$$\Psi'(x) = \frac{x J_1(x)}{J_0(x)}$$

$$q = \frac{\zeta^2}{2\sigma - 2}$$

$$\zeta = 2\pi f_o \frac{\rho(2 + 2\sigma)}{E}$$

and where $J_n(x)$ is the Bessel function of first kind of order $n$, $f_o$ is the resonance frequency, and $\rho$, $\sigma$ and $E$, are the density, Poisson ratio, and Young’s modulus, respectively, of the disk structural material.
IV. NICKEL DISK FABRICATION PROCESS

The fabrication process used for the solid-dielectric gap nickel resonators of this work is presented in Fig. 5, which shows cross-sections and associated scanning electron micrographs (SEM’s), taken along the AB plane of Fig. 3. The process begins with the wet oxidation growth of a 2 μm layer of oxide to provide electrical isolation between the conductive silicon substrate and the eventual nickel resonators and interconnects. 15 nm/120 nm/15 nm-thick Ti/Ni/Ti metal interconnects are then evaporated and patterned via lift-off, then buried under 400 nm of evaporated aluminum to achieve the cross-section shown in Fig. 5(a). The aluminum serves as a sacrificial layer to be removed later in the process. After wet etching anchor vias into the sacrificial aluminum using a solution of H₃PO₄/HNO₃/H₂O, 20 nm of nickel is evaporated over the entire wafer to serve as a seed layer for subsequent nickel plating. 3 μm of nickel structural material is then electroplated in a 50°C solution of nickel sulfate through a photoresist mold defining the disk structure. The mold and seed layer are then stripped using a PRS-2000 solution and a custom-made CH₃COOH/HNO₃/H₂SO₄/H₂O nickel etchant solution [17], respectively, to achieve the cross section of Fig. 5(b).

At this point, the disk structure is fully formed. It should be noted that the etch time in the seed-removing nickel etchant should be minimized, since this etchant also attacks the structural material, and will degrade the Q and stability of eventual μresonators if allowed to etch for lengthy periods. Regarding the 3 μm thickness of the disk, it should be noted that much thicker nickel films are possible via electroplating. In particular, films up to 1000 μm are possible using the Lithographie Galvanik Abformung (LIGA) process [18].

After the disk structures have been formed and the photoresist molds removed, a 30 nm-thick film of PECVD silicon nitride is then deposited at 380°C conformally over all surfaces, including and especially over the disk sidewalls, where it serves as the electrode-to-resonator gap material. At present, this nitride deposition represents the highest temperature step in the process. If 380°C is too high, then other methods for growing conformal dielectric films are available, such as atomic layer deposition (ALD), which can grow thin dielectric films at temperatures as low as 100°C [19].

To prepare for electrode formation, a combination of dry and wet etches are first used to expose interconnects and bond pads, yielding the cross-section of Fig. 5(c). Electrode construction then starts with the evaporation of another

20 nm-thick film of nickel to serve as another seed layer, this time for eventual electrode electroplating. To prevent plating over the top of disk structure when electrodes are plated, the nickel seed layer material atop the disk must first be removed. Pursuant to this, 13 μm of AZ9260 photoresist is spun and etched back by an O2-based reactive ion etch (RIE) to expose the seed layer atop the structure, yielding the cross-section shown in Fig. 5(d). The “top” seed layer is then removed by wet-etching using the solution of [17], leaving nickel seeding material still over the electrode regions. The photoresist is then stripped, and a new film of 6 μm thick AZ9260 photoresist is spun, exposed, and patterned, to form a mold defining one side of the electrodes. (The other side is defined by the nitride-encapsulated disk edges.) Nickel is again electroplated 3 μm-thick to achieve the cross-section of Fig. 5(e). Finally, the structure is released in 50°C sodium hydroxide solution, which provides high aluminum-to-nickel and aluminum-to-nitride selectivity. The final cross section is shown in Fig. 5(f).

Although the process flow of Fig. 5 features a solid-dielectric gap between the resonator and electrode, the process can easily be transformed to yield air-gap devices by merely including a step to remove the dielectric gap film between the electrode and resonator. In this case, the process would be simpler if the gap material were oxide instead of nitride, since then the gap could be cleared during the device release step.

Figure 6(a) presents the wide-view SEM of a fabricated and released nickel disk resonator with an 18 μm disk radius and 1.5 μm stem radius. The inset in (b) zooms in on the 30 nm nitride gap, revealing a problem with electrode formation during processing. In particular, rather than sitting flush against the disk sidewalls, the electrodes have been undercut near the device so that the actual electrode-to-resonator overlap is much smaller than intended. These imperfect electrodes effectively reduce the overall drive and sense electrode overlap area in fabricated devices, which then makes measurement of these resonators a bit more difficult than usual (but not impossible).

V. EXPERIMENTAL RESULTS

Table I summarizes the nickel wine-glass mode disk resonator designs evaluated in this work. Disk devices designed to these specifications were fabricated using the process flow described in Section IV, then tested in a custom-built chamber that could be pumped down to pressures below 50 μTorr. Pressures this low were needed to eliminate the influence of viscous gas damping on the $Q$’s of the resonator devices [20], thereby eliminating gas damping as a mechanism for $Q$-limiting, and hence, better elucidating other loss mechanisms. For testing, each die containing many resonator devices was mounted on a printed circuit board that was then placed into the test chamber. Individual devices were accessed by bond wiring to metal traces on the board, and air-tight electrical feedthroughs into and out of the vacuum chamber provided both DC and coaxial electrical interconnections between the board and external measurement instrumentation.

A. Nickel CC-Beams

In addition to wine-glass disks, CC-beam resonators were also included in the process run in order to gauge the material quality of present fabrication run versus ones that had been run previously [14]. Figures 7(a) and (b) present the SEM and measured frequency characteristic for a 6.6-MHz vertically actuated clamped-clamped beam resonator measured using the mixing measurement technique of [21].

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Center Stem Radius, 2μm</th>
<th>Center Stem Radius, 1.5μm</th>
<th>Stemless</th>
<th>Units</th>
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<td>18, 3</td>
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<tr>
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<td>30</td>
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<td>nm</td>
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<td>9</td>
<td>5</td>
<td>V</td>
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<td>3.56</td>
<td>1.59</td>
<td>V</td>
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<tr>
<td>LO Carrier Amplitude: $V_{LO}$</td>
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<td>6.25</td>
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<td>V</td>
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<tr>
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<td>59.65</td>
<td>59.96</td>
<td>MHz</td>
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<tr>
<td>Quality Factor In Vacuum: $Q$</td>
<td>490</td>
<td>5,077</td>
<td>54,507</td>
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</tbody>
</table>

Fig. 7: (a) SEM and (b) measured frequency characteristic for a 6.6-MHz vertically actuated clamped-clamped beam resonator measured using the mixing measurement technique of [21].
device’s tiny motional current from feedthrough parasitic currents that might otherwise mask it, the mixing measurement scheme of [21] was used to detect and plot the device response, shown in Fig. 7(b). Because the mixing measurement method of [21] is used, the units of the y-axis of the measurement run, the side-supported devices of this work were not testable, and only stem-supported devices were available for testing. In addition, due to the incomplete electrode-to-resonator overlap shown in Fig. 6, the mixing measurement scheme of [21] was again required for measurement of disk devices.

Since the anchor dissipation of previous stem-supported disk resonators was found to be a strong function of stem radius [23], insights into the impact of anchor losses on nickel resonator Q can be obtained by merely measuring devices with different stem radii. Pursuant to this, Figure 8 presents the measured frequency characteristic for an 18 μm-radius nickel wine-glass disk with a rather large 2 μm-radius center stem, showing a Q of only 490 at 60 MHz. This measurement was repeated over more than ten resonators with this stem size, with all devices exhibiting Q’s under 500. Assuming a nickel density equal to the bulk value of 8.9 kg/m³, the 60 MHz frequency of this device implies a Young’s modulus of 195 GPa and acoustic velocity of 4,678 m/s for the plated nickel material of this work.

Next, a 1.5 μm-radius center stem wine-glass disk resonator was tested. Figure 9 presents the measured frequency characteristic, which again shows resonance at 60 MHz, but this time with a Q in excess of 5,077. Clearly, the size of the center stem is a dominant factor governing the Q of a nickel wine-glass disk. If so, then the maximum Q of such a disk should ensue if the center stem can be eliminated. As mentioned, the side-supported devices, which were devoid of center stems, were not functional due to fabrication issues in this particular run; so stemless side-supported devices were not available.

C. Stemless Nickel Wine-Glass Disks

Fortunately, there were devices on this run that had neither stems, nor side supports. These devices were designed to be supported only by the solid material in their electrode-to-resonator gaps. Such a device, however, poses some measurement difficulty, since it does not have a lead with which to apply a dc-bias \( V_P \) to its conductive disk structure.
Fortunately, such a lead is not needed, since charge biasing can be used [24], where charge is deposited onto the resonator structure using a probe. From [24], the discharge time for a charge-biased resonator can be quite long, on the order of 900s before the output signal for a stand-alone resonator is attenuated by 3dB.

In the laboratory, testing of charge-biased resonators was done via the procedure summarized in Fig. 10. Here, all electrodes surrounding the disk resonator are first grounded, then a dc bias voltage \( V_{\text{charge}} \) is applied to the electrode underneath the disk structure. Next, a probe tip biased to \( V_{\text{charge}} \) is gently touched to the side wall of the conductive disk structure, charging it to \( V_{\text{charge}} \). Upon pulling the probe tip away from the disk, the charge remains on the structure for a time governed by its leakage rate, which from [24], has a time constant on the order of 40 minutes—plenty long enough to take several high resolution measurement sweeps. Upon charging the resonator, the printed circuit board on which the resonator die is mounted is then transferred from the probe station into the custom built vacuum chamber and hooked up to the electrical feedthroughs. Mixing measurement [21] then proceeds as usual, except that the local oscillator and RF signals must now be combined at the input, rather than added to the dc-bias port of previous renditions.

Using this procedure, Figure 11 presents the measured frequency characteristic for an 18 \( \mu \text{m} \)-radius 60 MHz nickel disk with no center stem support, suspended only by the nitride material in the electrode-to-resonator gap. As shown, the disk exhibits a measured \( Q \) of 54,507, which clearly exposes the enormous impact of the stem on nickel resonator \( Q \), and suggests that the nickel material quality is on par with other popular micromachinable materials (e.g., polysilicon, polydiamond) from a \( Q \) perspective. In fact, to our knowledge, the vacuum frequency-\( Q \) product of \( 3.3 \times 10^{12} \) attained by this particular nickel resonator is the highest yet seen for any micro-scale metal resonator in the VHF range.

The use of charge biasing in the last experiment is intriguing, but begs the question: How long before leakage currents discharge the effective charge-bias to the point of noticeable performance degradation? To answer this, Figure 12 presents a measured plot of resonant peak height versus time. As shown in the figure, leaky discharging of the resonant structure in vacuum attenuates the output signal by 3 dB after 30 minutes, which is plenty long enough to make measurements, and long enough to allow charge refreshing via a very simple, low-power circuit.

VI. CONCLUSIONS

Wine-glass nickel micromechanical disk resonators with nitride dielectric capacitive transducer gaps have been demonstrated with frequency-\( Q \) products as high as \( 3.3 \times 10^{12} \),...
which is the highest to date for any micro-scale metal resonator device in the VHF range. This result proves that the intrinsic Q limit of plated nickel thin-films is more than adequate for VHF filtering and oscillator applications, and on par with that of other popular micromachinable materials, including polysilicon. Table II summarizes the material properties of the nickel material used in this work and compares them to that of other popular MEMS materials. As shown, the acoustic velocity of this work’s nickel is 4,678 m/s, which is 1.71X smaller than that of polysilicon. However, high frequency devices should still be achievable. For example, for 1GHz, the needed radii of polysilicon and nickel disk resonators operating in their second radial-contour modes are 7.2 μm and 4.2 μm, respectively, which are on the same order and both easily achievable with present-day lithographic technology. The above characteristics, combined with the only 50°C plating temperature required for its deposition, makes nickel a very strong candidate for use in modular MEMS-transistor merging processes seeking to integrate MEMS over finished foundry transistors in a fully monolithic, post-transistor fashion.

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References.