Enter the names and SIDs for you and your lab partner into the boxes below.

Name 1	SID 1
Name 2	SID 2

## **Boost Converters**

We have tried to use resistors (voltage dividers) to transform voltages but found that these solutions suffer from very poor efficiency: A significant fraction of the total power is dissipated in the resistors and is not available for the load. Moreover, dividers are limited to lowering the voltage. This is problematic in many applications such as micro-mechanical actuators (MEMS) that often require high voltages for operation.

With inductors and capacitors we can overcome both problems. Since these elements (ideally) only store but do not dissipate energy, much higher efficiencies are attainable.

In this laboratory we will design and test a special kind of switching power supply called boost converter that raises the input voltage to a higher value and dimension the circuit to generate 15 V from a 5 V input. Figure 1 shows the schematic diagram. The device labeled IRF510 is a transistor. Download its datasheet from the course web. The diode conducts current only in the direction of the arrow.

To analyze the circuit we assume first that it is working correctly, in particular that the output voltage is 15 V. We will later verify of course that this is indeed the case. The voltage  $V_c$  is a pulse train and changes between 0 V and 5 V. For  $V_c = 5$  V the transistor (IRF510) is on and behaves essentially like a short circuit. Then  $V_{boost} = 0$  V and  $V_{diode} = V_{boost} - V_{out} = -15$  V. Since  $V_{diode}$  is negative, the diode does not conduct any current, i.e. it behaves like an open circuit. With  $V_c = 0$  V the situation reverses: now the transistor is off and the diode conducts. Figure 2 illustrates the two situations.

In situation (a),  $V_c = 5$  V, the supply voltage  $V_{in}$  appears across the inductor. From the differential equation for inductance we observe that inductors integrate voltage. Therefore the inductor current  $I_L$  is a ramp with slope determined by  $V_{in}$  and L. In situation (b) the inductor again integrates the voltage  $V_{in} - V_{out} = -10$  V that appears across it. Since the voltage is negative the current through the inductor decreases. In steady state the current increase and decrease must be identical, as shown in Figure 3; otherwise the average current would continually increase or decrease.

Since voltage is proportional to the slope of the current, we note intuitively that reducing the ratio of  $T_{off}/T_{on}$  results in a higher output voltage  $V_{out}$ . This is because the positive slope is proportional to  $V_{in}$  and the negative slope of the decreasing current is proportional to  $V_{out} - V_{in}$ . In the laboratory we will analyze this relationship quantitatively.

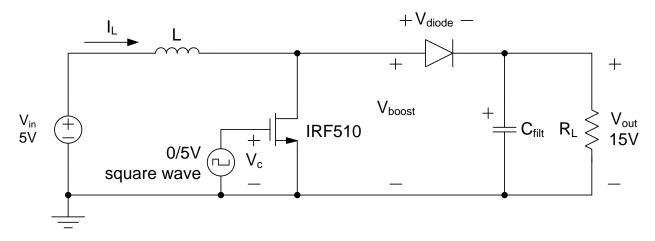


Figure 1 Boost converter.

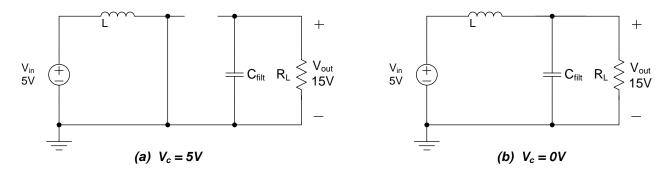


Figure 2 Boost converter operating principle with the switch (transistor) closed (a) and open (b).

## Design

Let's first derive an expression for the voltage boost factor,  $V_{out}/V_{in}$ . We start by writing expressions for  $\Delta I_L$  during  $T_{on}$  and  $T_{off}$ . At this point, enter only the expressions. Once you have determined the value of L (see below) you can solve for and enter the numerical answer. Same for the simulation result. Hint: set up the differential equation for current and voltage in the inductor during the two phases.

		Expression		Simulation		Calculation	
During T	Δ <i>Ι</i>	•	1 pt.		1 pt.		1 pt.
During T <sub>on</sub>	$\Delta I_L =$		4		5		0
During T "	ΔI. —		1 pt.		1 pt.		1 pt.
During $T_{off}$	$\Delta T_{L} =$		6		7		1

From the timing diagram shown in the guide we know that the magnitude of  $\Delta I_L$  is the same during  $T_{on}$  and  $T_{off}$ . Equate the equations above and solve for the voltage boost factor  $V_{out}/V_{in}$ .

Remarkably this result depends only on  $T_{on}$  and  $T_{off}$  and is independent of the value of the inductance. Calculate  $T_{on}/T_{off}$  for  $V_{out} = 15$  V and  $V_{in} = 5$  V.

$$T_{on}/T_{off} =$$

For simplicity, in this laboratory we will generate  $T_{on}$  and  $T_{off}$  with the pulse generator. More practical implementations adjust this ratio dynamically to keep the value of  $V_{out}$  constant in the presence of variations of  $V_{in}$  and the load current. Calculate the value of  $T_{on}/T_{off}$  that keeps  $V_{out}$  constant despite varying  $V_{in}$ .

$V_{in} =$	V	$T_{on}/T_{off} =$	1 3
$V_{in} =$	V	$T_{on}/T_{off} =$	1 4

To finalize the design of the boost converter we must determine the operating frequency f = 1/T with  $T = T_{on} + T_{off}$  and the values of *L* and  $C_{filt}$ . We pick f = 100 kHz to account for the frequency limitation of solderless breadboards<sup>1</sup>. From this we can calculate  $T_{on}$  and  $T_{off}$  and then solve for *L* from one of the equations for  $\Delta I_L = 6$  mA. Round *L* to the nearest available value (use the resistor scale, i.e. multiples of 10, 12, 15, etc).

During  $T_{on}$  the diode is not conducting and the entire current to the load comes from  $C_{filt}$ . Because of this the output voltage will drop. Keeping this drop to  $\Delta V_{out} = \text{mV}$  for  $R_L = 1 \text{ k}\Omega$ , determine the value of  $C_{filt}$  (use the next larger available value in the lab). Realizing that  $\Delta V_{out} \ll V_{out}$  we conclude that the current through the resistor is approximately constant,  $I_{RL} = V_{out}/R_L$ .

$$C_{filt} =$$

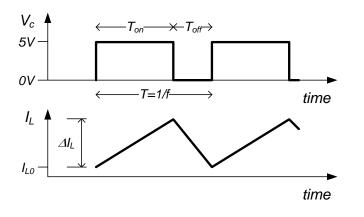
L =

Verify your result with SPICE. For simulation only, add a  $6\Omega$  resistor in series with the inductor to account for the winding resistance (do not add this resistor in the actual circuit you will be building). Attach a transient simulation showing  $V_c$ ,  $V_{boost}$ ,  $V_{diode}$ ,  $V_{out}$  and the current through the inductor for 3 cycles in steady state to your lab report (4 points; no credit for lab reports without simulation).

Simulated  $V_{boost} =$ 

Now you are ready to test the boost converter in the laboratory. Although it is designed to generate only 15 V, it can produce voltages in excess of 30 V, e.g. when the input voltage is higher than 5 V. Exert extra caution and touch circuit nodes only after having determined (e.g. with the oscilloscope) that voltage levels agree with your simulation results and are below 30 V. Also, complete the entire circuit before turning on power. Do not omit the diode and load resistor. Measure  $V_c$ ,  $V_{boost}$ ,  $V_{diode}$ ,  $V_{out}$  with the oscilloscope and compare your result to SPICE. Comment on any discrepancies (hint: consider the assumptions made for the calculations).

<sup>&</sup>lt;sup>1</sup>Switching power supplies are usually operated at higher frequencies to reduce the size of the inductor.

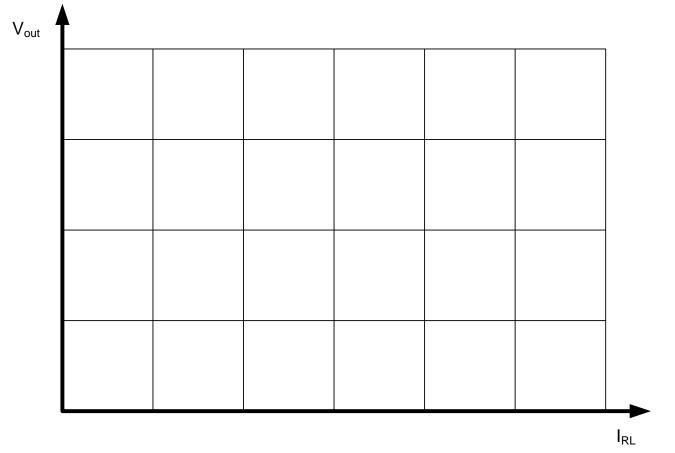




Explain discrepancies between calculations, simulations, and measurements:

3 pts. 9

In SPICE and the actual circuit, vary the load resistor  $R_L$  from  $100 \Omega$  to  $20 \text{ k}\Omega$  and graph your result. Label the axes!



Demo the circuit to the GSI.

Ideally the voltage should be independent of  $V_{in}$  and the current  $I_{RL}$  through the resistor. In practice it drops because of the constant boost factor and the series resistance of the inductor and diode, and the finite on-resistance of the transistor. Practical implementations of boost converters include additional circuitry that monitors the output voltage and dynamically adjusts  $T_{on}/T_{off}$  to ensure a constant  $V_{out}$ .

Increase the input voltage to  $V_{in} = V$ . What are the calculated, simulated (you need to rerun SPICE), and measured values of  $V_{out}$ ? Use the same value for  $T_{on}/T_{off}$  as before.

	Measured		Simulated		Calculated	
17		1 pt.		1 pt.		1 pt.
$V_{out} =$		11		12		7

Now vary  $T_{on}$  (without changing the frequency f) to adjust  $V_{out}$  back to 15 V. Compare measurement results with your understanding of the circuit. Fill in the calculated, simulated, and measured values of  $T_{on}$  and  $T_{off}$  that restore  $V_{out}$  to its design value while keeping f constant.

	Measured	Simulated	Calculated	
т —		1 pt.	1 pt.	1 pt.
$T_{on} =$		13	14	8
T		1 pt.	1 pt.	1 pt.
$T_{off} =$		15	16	9

Explain discrepancies between measured, simulated, and calculated results.

Ask the GSI to verify your circuit.

## What Have We Learned?

We learned how to use an inductor (and a clock) to boost voltage. Similar circuits are used in power bricks and you won't believe it—flashlights. Many small high-tech flashlights operate off a single AA battery. The voltage is too low to drive an LED. A boost converter solves this problem.

Although boost and buck converters do not "generate" power, these efficient circuits have significantly lower power dissipation than other solutions such as transformers or resistive dividers. Of course, the switch control and voltage regulation circuits used in complete converters still dissipates energy and contribute to global warming. Let's hope the attached devices are used for a good purpose.

Password: