Instruction status						Execution	Write			
Instruction		*j*	*k*	*iteration*	*Issue*	*complete*	*Result*		*Busy*	*Address*
LD	F2	0	R1	1				Load1	No	
MULTD	F4	F2	F0	1				Load2	No	
LD	F6	0	R2	1				Load3	Yes	*Qi*
ADDD	F6	F4	F6	1				Store1	No	
SD	F6	0	R2	1				Store2	No	
LD	F2	0	R1	2				Store3	No	
MULTD	F4	F2	F0	2						
LD	F6	0	R2	2						
ADDD	F6	F4	F6	2						
SD	F6	0	R2	2						

Reservation Stations				*S1*	*S2*	*RS for j*	*RS for k*					
Time	*Name*	*Busy*	*Op*	*Vj*	*Vk*	*Qj*	*Qk*	*Code:*				
	Add1	No						LD	F2	0	R1	
	Add2	No						MULT	F4	F2	F0	
	Add3	No						LD	F6	0	R2	
	Mult1	No						ADDD	F6	F4	F6	
	Mult2	No						SD	F6	0	R2	
								ADDI	R1	R1	#8	
								ADDI	R2	R2	#8	
								SGTI	R3	R1	done	
								BEQZ	R3	foo		

Register result status												
Clock	**R1**	**R2**		*F0*	*F2*	*F4*	*F6*	*F8*	*F10*	*F12*	...	*F30*
0	**0**	**0**	*Qi*									

Instruction status

Instruction		j	k	iteration	Issue	Executic complet	Write Result
LD	F2	0	R1	1			
MULTD	F4	F2	F0	1			
LD	F6	0	R2	1			
ADDD	F6	F4	F6	1			
SD	F6	0	R2	1			
LD	F2	0	R1	2			
MULTD	F4	F2	F0	2			
LD	F6	0	R2	2			
ADDD	F6	F4	F6	2			
SD	F6	0	R2	2			

	Busy	Address
Load1	No	
Load2	No	
Load3	Yes	Qi
Store1	No	
Store2	No	
Store3	No	

Reservation Stations

Time	Name	Busy	Op	S1 Vj	S2 Vk	RS for j Qj	RS for k Qk
	Add1	No					
	Add2	No					
	Add3	No					
	Mult1	No					
	Mult2	No					

Code:

```
LD    F2   0  R1
MULT  F4   F2 F0
LD    F6   0  R2
ADDI  F6   F4 F6
SD    F6   0  R2
ADDI  R1   R1 #8
ADDI  R2   R2 #8
SGTI  R3   R1 done
BEQZ  R3   foo
```

Register result status

Clock	R1	R2	F0	F2	F4	F6	F8	F10	F12	...	F30
		Qi									

Instruction status

Instruction		j	k	iteration	Issue	Execution complete	Write Result
LD	F2	0	R1	1			
MULTD	F4	F2	F0	1			
LD	F6	0	R2	1			
ADDD	F6	F4	F6	1			
SD	F6	0	R2	1			
LD	F2	0	R1	2			
MULTD	F4	F2	F0	2			
LD	F6	0	R2	2			
ADDD	F6	F4	F6	2			
SD	F6	0	R2	2			

	Busy	Address	
Load1	No		
Load2	No		
Load3	Yes	*Qi*	
Store1	No		
Store2	No		
Store3	No		

Reservation Stations

Time	Name	Busy	Op	S1 Vj	S2 Vk	RS for j Qj	RS for k Qk
	Add1	No					
	Add2	No					
	Add3	No					
	Mult1	No					
	Mult2	No					

Code:

```
LD    F2   0  R1
MULT  F4   F2 F0
LD    F6   0  R2
ADDD  F6   F4 F6
SD    F6   0  R2
ADDI  R1   R1 #8
ADDI  R2   R2 #8
SGTI  R3   R1 done
BEQZ  R3   foo
```

Register result status

Clock	R1	R2	F0	F2	F4	F6	F8	F10	F12	...	F30
		Qi									

Instruction status

Instruction		j	k	iteration	Issue	Executic complet	Write Result
LD	F2	0	R1	1			
MULTD	F4	F2	F0	1			
LD	F6	0	R2	1			
ADDD	F6	F4	F6	1			
SD	F6	0	R2	1			
LD	F2	0	R1	2			
MULTD	F4	F2	F0	2			
LD	F6	0	R2	2			
ADDD	F6	F4	F6	2			
SD	F6	0	R2	2			

	Busy	Address	
Load1	No		
Load2	No		
Load3	Yes		Qi
Store1	No		
Store2	No		
Store3	No		

Reservation Stations

Time	Name	Busy	Op	S1 Vj	S2 Vk	RS for j Qj	RS for k Qk
	Add1	No					
	Add2	No					
	Add3	No					
	Mult1	No					
	Mult2	No					

Code:

```
LD    F2   0  R1
MULT  F4   F2 F0
LD    F6   0  R2
ADDI  F6   F4 F6
SD    F6   0  R2
ADDI  R1   R1 #8
ADDI  R2   R2 #8
SGTI  R3   R1 done
BEQZ  R3   foo
```

Register result status

Clock	R1	R2		F0	F2	F4	F6	F8	F10	F12	...	F30
			Qi									