PAPER Special Section of Papers Selected from JTC-CSCC'93

Communication Systems via Chaotic Modulations

Makoto ITOH[†], Hiroyuki MURAKAMI [†] and Leon O. CHUA^{††}, Members

SUMMARY New communication systems via chaotic modulations are experimentally demonstrated. They contain the well-known chaotic circuits as its basic elements -- Chua's circuits and canonical Chua's circuits. The following advantage is found in our laboratory experiments: (a) Transmitted signals have broad spectra. (b) Secure communications are possible in the sense that the better parameter matching is required in order to recover the signal. (c) The circuit structure of our communication system is most simple at this stage. (d) The communication systems are easily built at a small outlay.

key words: chaos, synchronization, communication systems, Chua's circuit

1. Introduction

Recently, Pecora and Carroll demonstrated the possibility of synchronizing chaotic systems [1]. In this paper, we propose new communication systems as the possible application of chaotic synchronization [2]. Considering their complicated behavior, chaotic signals are the candidates masking and scrambling the signals [3],[4]. The main idea is to use the chaotic modulation to transmit the informational signals. And the chaotic synchronization mechanism is used to recover the signals [5]-[7].

Next, we show the basic idea to construct the communication systems. First, let us consider the chaotic dynamical system

$$\frac{dx}{dt} = h(x,y), \ \frac{dy}{dt} = k(x,y), \tag{1}$$

where (x, y) is the n+m-dimensional space, and h and k are smooth functions of x and y. Then the transmitting system is given by

$$\frac{dx}{dt} = h(x,y) + s(t), \quad \frac{dy}{dt} = k(x,y), \tag{2}$$

where s(t) is the informational signal and x(t) is the chaotically modulated transmitted signal. That is, the noise-like masking signal x(t) is transmitted to the channel.

The receiver constructs the following system by using the transmitted signal x(t):

Manuscript received October 18, 1993.

Manuscript revised February 7, 1994.

^{††}The author is with the Department of Electrical Engineering and Computer Sciences, University of California, Berkeley, CA 94720, USA.

$$\frac{dz}{dt} = k(x, z), \ r(t) = \frac{dx}{dt} - h(x, z). \tag{3}$$

The first equation is the response system driven by the signal x(t). The second equation can retrieve the signal s(t), that is, r(t) is the recovered signal. This is due to the following reason. If all the Lyapunov exponents of the z subsystem are negative, then the y subsystem and the z subsystem will synchronize, that is, $|y(t)-z(t)| \to 0$ as $t \to \infty$ (For details, see Ref. [1]). Thus s(t) is recovered as:

$$r(t) = \frac{dx}{dt} - h(x, z) \to \frac{dx}{dt} - h(x, y) = s(t)$$
 for $t \to \infty$. (4)

We examine the above idea by using Chua's circuits and canonical Chua's circuits.

2. Chua's Circuits and Canonical Chua's Circuits

Chua's circuit is a remarkably simple and robust circuit, which consists of a linear inductor L, a linear resistor R, two linear capacitors C_1 and C_2 , and a nonlinear resistor N_R (see Fig. 1 and also Ref. [8]). The state equations are given by

$$C_{1}\frac{dv_{1}}{dt} = \frac{v_{2} - v_{1}}{R_{1}} - g(v_{1}),$$

$$C_{2}\frac{dv_{2}}{dt} = \frac{v_{1} - v_{2}}{R_{1}} + i_{1},$$

$$L_{1}\frac{di_{1}}{dt} = -v_{2} - r_{1}i_{1},$$
(5)

where $g(\cdot)$ is a piecewise-linear function defined by

$$g(v_1) = G_b v_1 + 0.5(G_a - G_b)(|v_1 + B_p| - |v_1 - B_p|), (6)$$

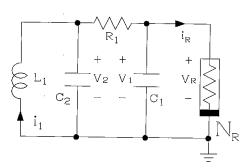


Fig. 1 Chua's circuit.

[†]The authors are with the Faculty of Engineering, Nagasaki University, Nagasaki-shi, 852 Japan.

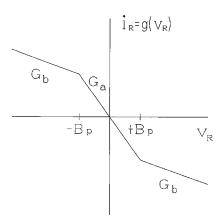


Fig. 2 The v-i characteristic of N_R in the Chua's circuit.

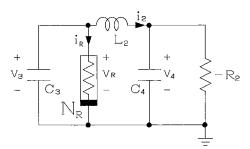


Fig. 3 Canonical Chua's circuit.

and shown in Fig. 2. The term r_1i_1 is added to the ideal Chua's equations in order to account for the small inductor resistance in the physical circuit. The parameters we use are

$$\begin{split} &C_1 = 10.3\,\mathrm{nF},\ C_2 = 97.4\,\mathrm{nF},\\ &L_1 = 20.7\,\mathrm{mH},\ R_1 = 1.41\,\mathrm{k}\Omega,\\ &B_p = 1.85\,\mathrm{V},\ G_a = -0.87\,\mathrm{mS},\\ &G_b = -0.52\,\mathrm{mS},\ \mathrm{and}\ r_1 = 64.2\Omega. \end{split}$$

With these values, (5) has a double scroll attractor.

Similarly, the state equations for the canonical Chua's circuit shown in Fig. 3 is given by

$$C_{3} \frac{dv_{3}}{dt} = -f(v_{3}) - i_{2},$$

$$C_{4} \frac{dv_{4}}{dt} = \frac{v_{4}}{R_{2}} + i_{2},$$

$$L_{2} \frac{di_{2}}{dt} = v_{4} - v_{3} - r_{2}i_{2},$$
(7)

where $f(\cdot)$ is a piecewise-linear function defined by

$$f(v_3) = G_b'v_3 + 0.5(G_a' - G_b')(|v_3 + B_n'| - |v_3 - B_n'|), (8)$$

and r_2 is the inductor resistance (See Fig. 4 and also Ref. [9]). To generate a chaotic attractor, we used

$$C_3 = 14.9 \,\mathrm{nF}, \ C_4 = 31.9 \,\mathrm{nF},$$

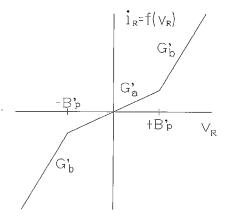


Fig. 4 The v-i characteristic of N_R in the canonical Chua's circuit.

$$\begin{split} L_2 &= 20.7\,\mathrm{mH},\ R_2 = 1.00\,\mathrm{k}\Omega,\\ B_p' &= 0.97\,\mathrm{V},\ G_a' = 0.45\,\mathrm{mS},\\ G_b' &= 4.00\,\mathrm{mS},\ \mathrm{and}\ r_2 = 52.5\Omega. \end{split}$$

3. Communication Systems

The basic construction of our communication system for Chua's circuit is shown in Fig. 5. The circuit equations for the transmitting system are given by

$$C_{1}\frac{dv_{1}}{dt} = \frac{v_{2} - v_{1}}{R_{1}} - g(v_{1}) + \frac{e(t) - v_{1}}{R_{S}},$$

$$C_{2}\frac{dv_{2}}{dt} = \frac{v_{1} - v_{2}}{R_{1}} + i_{1},$$

$$L_{1}\frac{di_{1}}{dt} = -v_{2} - r_{1}i_{1}.$$
(9)

We use the voltage source e(t) as an informational signal, and $v_1(t)$ as a transmitted signal. That is, $v_1(t)$ is a chaotically modulated transmitted signal.

The circuit equations for the receiving system are given by

$$C_{1} \frac{dv'_{1}}{dt} = \frac{v'_{2} - v'_{1}}{R_{1}} - g(v'_{1}) - \frac{v'_{1}}{R_{S}},$$

$$C_{2} \frac{dv'_{2}}{dt} = \frac{v'_{1} - v'_{2}}{R_{1}} + i'_{1},$$

$$L_{1} \frac{di'}{dt} = -v'_{2} - r_{1}i'_{1},$$
(10)

where $v_1(t) = v_1'(t)$ because of the voltage buffer.

Next, we show how the informational signal can be recovered (demodulation process). From the first equation in (9), we obtain

$$e(t) = R_S \left[C_1 \frac{dv_1}{dt} - \frac{v_2 - v_1}{R_1} + g(v_1) + \frac{v_1}{R_S} \right].$$
 (11)

Similarly, the current j(t) in Fig. 5 is given by

$$j(t) = \left[C_1 \frac{dv_1'}{dt} - \frac{v_2' - v_1'}{R_1} + g(v_1') + \frac{v_1'}{R_S}\right]. \tag{12}$$

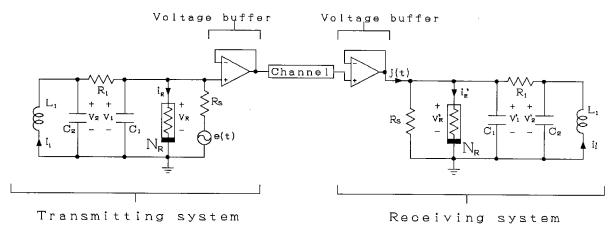


Fig. 5 Communication system which utilizes Chua's circuits.

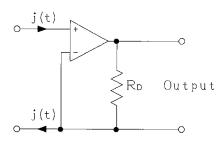


Fig. 6 Current detector.

Establishing the difference $p(t) = v_2(t) - v_2'(t)$ and q(t) = i(t) - i'(t), we get

$$C_2 \frac{dp}{dt} = -\frac{p}{R_1} + q,$$

$$L_1 \frac{dq}{dt} = -p - rq.$$
(13)

Since the origin of (13) is globally asymptotically stable, we conclude that $|p| = |v_2 - v_2'| \to 0$ and $|q| = |i - i'| \to 0$ as $t \to \infty$. That is, the (v_2, i) -subsystem and the (v_2', i') -subsystem will synchronize. (We get the same results by calculating the Lyapunov exponents of the (v_2', i') -subsystem; see Ref. [1].) Thus, j(t) is given by:

$$j(t) = \left[C_1 \frac{dv_1'}{dt} - \frac{v_2' - v_1'}{R_1} + g(v_1') + \frac{v_1'}{R_S}\right] \to \frac{e(t)}{R_S}.$$
(14)

It implies that the current j(t) varies in proportion to the informational signal e(t). That is, the informational signal can be recovered by using the current detectors in Fig. 6. (The circuit parameters in our experiments are $R_S=33.9\,\mathrm{k}\Omega$ and $R_D=2.00\,\mathrm{k}\Omega$.)

The similar communication system is also given in Fig. 7, which utilizes canonical Chua's circuits. Its details are omitted here, since the basic idea of this communication system is as same as that of Chua's circuit.

4. Experimental Results

We built the two types of communication systems, that is, the communication system via Chua's circuits and the one via canonical Chua's circuits. Both systems are tested by using various kinds of signals, for example, human voices, music, and so on. Figures 8–11 and Figs. 12–15 show the experimental results for the Chua's circuit and those for the canonical Chua's circuit, respectively. From these results, we conclude the followings:

- (a) The communication systems are easily built at a small outlay.
- (b) The waveforms of the transmitted chaotic signal x(t) can mask the input signal s(t) if the amplitude of s(t) is small enough (see Fig. 8 and Fig. 12).
- (c) The transmitted signals have broad spectra, and also can mask the spectra of the input signals (see Fig. 9 and Fig. 13).
- (d) The informational signals are recovered with sufficient quality from the transmitted signals (see Fig. 10 and Fig. 14).
- (e) These systems have the high sensitivity of the parameter mismatch. (see Fig. 11 and Fig. 15). To eliminate the masking signal, very accurate knowledge of the parameter of the system is required to synchronize the chaotic signal. That is, the parameters of the systems serve as the "encryption key". In this sense, our communication systems are secure.

5. Transmission Methods

At this stage, three different methods have proposed for transmitting an informational signal s(t) via chaotic signal u(t). We discuss them briefly. The first, chaotic masking consists of adding s(t) to the chaotic signal u(t) [3], [4]. This method is very simple, but contains the following disadvantages:

(a) The power level of the informational signal must be significantly lower than that of the chaotic signal in

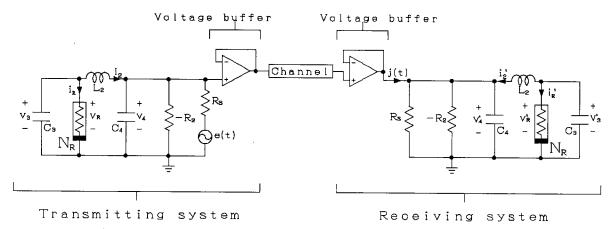


Fig. 7 Communication system which utilizes canonical Chua's circuits.

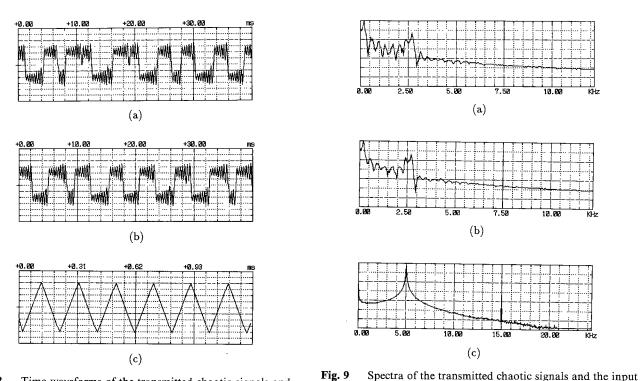


Fig. 8 Time waveforms of the transmitted chaotic signals and the input signal. (a) Transmitted chaotic signal with no input. Horizontal scale: 2.0 ms/div. Vertical scale: 1.0 V/div. (b) Transmitted chaotic signal with an input. Horizontal scale: 2.0 ms/div. Vertical scale: 1.0 V/div. (c) Input signal of frequency 5.0 kHz and amplitude 0.4 V. Horizontal scale: 0.062 ms/div. Vertical scale: 0.1 V/div.

order for the demodulation to be possible.

(b) The demodulation is disrupted when the informational signal approaches to the natural frequency of the LC tank in Chua's circuit [4].

In the second, chaotic switching [10]–[13], a binary signal drives a switch between two parameters values of the transmitting system. On the receiver side, two systems, one for each parameter value, try to synchronize to the transmitted signal. In this case, the system

input. Horizontal scale: 0.5 kHz/div. Vertical scale: 10 dB/div. (b) Spectrum of the transmitted chaotic signal with an input. Horizontal scale: 0.5 kHz/div. Vertical scale: 10 dB/div. (c) Spectrum of the input signal. Frequency of the input signal is 5.0 kHz. Amplitude of the input signal is 0.4 V. Horizontal scale:

signal. (a) Spectrum of the transmitted chaotic signal with no

1.0 kHz/div. Vertical scale: 10 dB/div.

has the following properties:

- (a) The informational signal is a digital signal.
- (b) The circuit structure of the receiving system is complicated.

Finally, chaotic modulation proposed in this paper uses directly the synchronization method of Ref. [2]. Our system has the following properties:

(a) The circuit structure of the communication system is most simple.

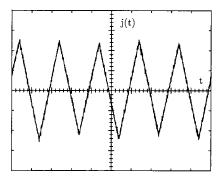


Fig. 10 Time waveform of the recovered signal. Horizontal scale: 0.1 ms/div. Vertical scale: 10 mV/div.

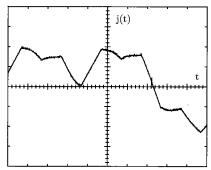


Fig. 11 Time waveform of the recovered signal with 3% resistor mismatch. Horizontal scale: 0.1 ms/div. Vertical scale: 50 mV/div.

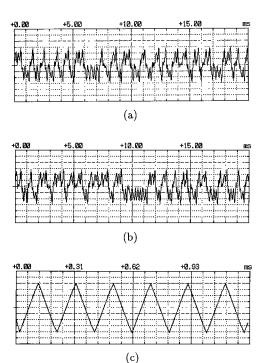


Fig. 12 Time waveforms of the transmitted chaotic signals and the input signal. (a) Transmitted chaotic signal with no input. Horizontal scale: 1.0 ms/div. Vertical scale: 0.5 V/div. (b) Transmitted chaotic signal with an input. Horizontal scale: 1.0 ms/div. Vertical scale: 0.5 V/div. (c) Input signal of frequency 5.0 kHz and amplitude 0.4 V. Horizontal scale: 0.062 ms/div. Vertical scale: 0.1 V/div.

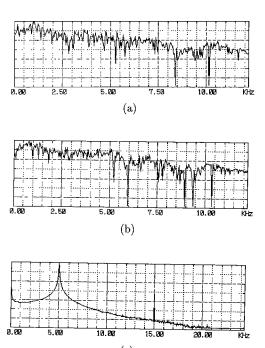


Fig. 13 Spectra of the transmitted chaotic signals and the input signal. (a) Spectrum of the transmitted chaotic signal with no input. Horizontal scale: 0.5 kHz/div. Vertical scale: 10 dB/div. (b) Spectrum of the transmitted chaotic signal with an input. Horizontal scale: 0.5 kHz/div. Vertical scale: 10 dB/div. (c) Spectrum of the input signal. Frequency of the input signal is 5.0 kHz. Amplitude of the input signal is 0.4 V. Horizontal scale: 1.0 kHz/div. Vertical scale: 10 dB/div.

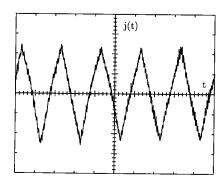


Fig. 14 Time waveform of the recovered signal. Horizontal scale: 0.1 ms/div. Vertical scale: 10 mV/div.

- (b) The informational signal s(t) is usually analog, but digitally modulated signals can also be used.
- (c) This method is easily applied to some other chaotic circuits.

In our transmitting system a signal injected into the chaotic circuit. This addition modifies the behavior of the circuit, and some care should be taken in order that the addition does not destroy the chaotic behavior and spectra. However, this method does not have the above mentioned disadvantage. Furthermore, this care makes the cording function used in Ref. [5] unnecessary. (The results in Ref. [5] were written in early stage of this

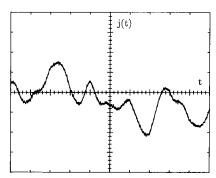


Fig. 15 Time waveform of the recovered signal with 3% resistor mismatch. Horizontal scale: 0.1 ms/div. Vertical scale: 50 mV/div.

work.)

6. Conclusion

We proposed the new communication systems via chaotic modulations and chaos synchronizations. Furthermore, three different transmission methods are discussed. The discrete version of our communication system is given in Refs. [14]–[16]. These systems have some advantages and disadvantages. The performance of the communication systems which are given in this paper and Refs. [14]–[16] will be discussed elsewhere.

Lastly, we refer the work of Ushio [17], in which a new communication system via neural networks is given. It may bring a new scheme in this field.

Acknowledgments

The authors would like to thank the reviewers for their helpful comments and suggestions. This work was supported in part by the Japanese Ministry of Education.

References

- [1] Pecora, L.M. and Carroll, T.L., "Synchronization in chaotic systems," *Phy. Rev. Lett.*, vol.64, no.8, pp.821–824, 1990
- [2] Chua, L.O., Kocarev, Lj., Eckert, K. and Itoh, M., "Experimental chaos synchronization in Chua's circuit," *Int. J. Bifurcation and Chaos*, vol.2, no.3, pp.705–718, 1992.
- [3] Oppenheim, A.L., Wornell, G.W., Isabelle, S.H. and Cuomo, K.M., "Signal processing in the context of chaotic signals," *Proc.* 1992 IEEE ICASSP, IV, pp.117–120, 1992.
- [4] Kocarev, Lj., Halle, K.S., Eckert, K., Parlitz, U. and Chua, L.O., "Experimental demonstration of secure communications via chaos synchronization," *Int. J. Bifurcation and Chaos*, vol.2, no.3, pp.709–713, 1992.
- [5] Halle, K.S., Wu, C.W., Itoh, M. and Chua, L.O., "Spread spectrum communication through modulation of chaos," *Int. J. Bifurcation and Chaos*, vol.3, no.2, pp.469–477, 1993.
- [6] Itoh, M., Murakami, H., Halle, K.S. and Chua, L.O., "Transmission of signals by chaos synchronization," *IEICE Technical Report*, CAS93-39, NLP93-27, Jun. 1993.

- [7] Itoh, M., Murakami, H., Halle, K.S. and Chua, L.O., "Communication systems via chaos synchronization," *Proc. of the JTC-CSCC'93*, pp.12–17, Jul. 1993.
- [8] Kennedy, M.P., "Robust op amp realization of Chua's circuits," *Frequent*, 46(3-4), pp.66-80, 1992.
- [9] Itoh, M. and Hayashi, S., "Attractors in an eventually bounded circuit," *IEICE Trans.*, vol.E71, no.8, pp.750–758, Aug. 1988.
- [10] Parlitz, U., Chua, L.O., Kocarev, Lj., Halle, K.S. and Shang, A., "Transmission of digital signals by chaotic synchronization," *Int. J. Bifurcation and Chaos*, vol.2, no.4, pp.973–977, 1992.
- [11] Kennedy, M.P. and Dedieu, H., "Experimental demonstration of binary chaos-shift-keying using self-synchronizing Chua's circuits," Workshop on Nonlinear Dynamics of Electronic Systems, Dresden, 1993.
- [12] Dedieu, H., Kennedy, M.P. and Hasler, M., "Chaos shift keying: Modulation of a chaotic carrier using selfsynchronizing Chua's circuits," *IEEE Trans. Circuits and* Systems (part II), vol.40, no.10, pp.634-642.
- [13] Hasler, M., Dedieu, H. and Kennedy, M.P., "Synchronization of chaotic signals," Workshop on Nonlinear Dynamics of Electronic Systems, Dresden, 1993.
- [14] Itoh, M. and Murakami, H., "Chaos synchronization and secure communications in discrete dynamical systems," *IEICE Technical Report*, NLP92-50, Dec. 1992.
- [15] Itoh, M. and Murakami, H., "Chaos synchronization in discrete-time dynamical systems and secure communication," Proc. of the 11th European Conference on Circuit Theory and Design, Davos, pp.611-614, 1993.
- [16] Itoh, M., Murakami, H. and Momiki, S., "Application of Yamakawa's chaotic chips to secure communications," Record of the Joint Conference of Electrical and Electronics Engineering in Kyushu, 1993.
- [17] Ushio, T., "Chaotic synchronization in one-dimensional chaotic neural networks with unidirectional connections," *IEICE Technical Report*, NLP93-50, Oct. 1993.



systems.

Makoto Itoh received the B.E., M.E., and Dr. E. degree in Computer Science and Communication Engineering from Kyushu University in 1977, 1979, and 1983, respectively. He was a Research Associate of Kyushu University from 1982 to 1984. In 1984, he joined Nagasaki University, where he is presently an Associate Professor of Electrical Engineering and Computer Science. His research interests are in nonlinear networks and dynamical



Hiroyuki Murakami was born in Nagasaki, Japan on Mar. 14, 1971. He received the B.E. in Electrical Engineering and Computer Science from Nagasaki University in 1993. He is now studying towards the M.E. degree in Nagasaki University. His research interests is in nonlinear dynamical systems.

Leon O. Chua received the S.M. degree from the Massachusetts Institute of Technology in 1961 and the Ph.D. degree from the University of Illinois, Urbana, in 1964. He was also awarded a Doctor Honoris Causa from the Ecole Polytechnique Federale-Lausanne, Switzerland, in 1983, an Honorary Doctorate from the University of Tokushima, Japan, in 1984, and an HONORARY DOCTORATE (Dr. Ing. E.h.) from the Techniche Universität Dresden in 1992. He is presently a professor of Electrical Engineering and Computer Sciences at the University of California, Berkeley. Professor Chua's research interests are in the areas of general nonlinear network and system theory. He has been a consultant to various electronic industries in the areas of nonlinear network analysis, modeling, and computer-aided design. He is the author of Introduction to Nonlinear Network Theory (New York: McGraw-Hill, 1969), and a coauthor of the books Computer-Aided Analysis of Electronic Circuits: Algorithms and Computational Techniques (Englewood Cliffs, NJ: Prentice-Hall, 1975), Linear and Nonlinear Circuits (New York: McGraw-Hill, 1987), and Practical Numerical Algorithms for Chaotic Systems (New York: Springer-Verlag, 1989). He has published many research papers in the area of nonlinear networks and systems. Professor Chua was elected Fellow of the IEEE in 1974. He served as Editor of the IEEE Transactions on Circuits and Systems from 1973 to 1975 and as the President of the IEEE society on Circuits and Systems in 1976. He is presently the editor of the International Journal of Bifurcation and Chaos, a deputy editor of the International Journal of Circuit Theory and Applications, and the Editor of the World Scientific Books Series on Nonlinear Science. Professor Chua is the holder of five U.S. patents. He is also the recipient of several awards and prizes, including the 1967 IEEE Browder J. Thompson Memorial Prize Award, the 1973 IEEE W.R.G. Baker Prize Award, the 1974 Frederick Emmons Terman Award, the 1976 Miller Research Professorship from the Miller Institute, the 1982 Senior Visiting Fellowship at Cambridge University, England, the 1982/83 Alexander von Humboldt Senior U.S. Scientist Award at the Technical University of Munich, W. Germany, the 1983/84 Visiting U.S. Scientist Award at Waseda University, Tokyo, from the Japan Society for Promotion of Science, the IEEE Contennial Medal in 1985, the 1985 Myril B. Reed Best Paper Prize, the 1985 and 1989 IEEE Cuillemin-Cauer Prize, and the 1993 IEEE CAS Technical Achievement Prize. In the fall of 1986, Professor Chua was awarded a Professor Invité International Award at the University of Paris-Sud from the French Ministry of Education. In the summer of 1993, he was awarded a Fellowship from the Départment de Mathématique, Université de Nice.